

Amitabh Chatterjee

List of Publications by Year in descending order

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22
papers

144
citations

1163117

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1281871

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22
all docs

22
docs citations

22
times ranked

141
citing authors

#	ARTICLE	IF	CITATIONS
1	Bipolar effects in snapback mechanism in advanced n-FET transistors under high current stress conditions. Journal of Physics Communications, 2020, 4, 065009.	1.2	0
2	Magnetic Field-Assisted Radiation Enhancement From a Large Aperture Photoconductive Antenna. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 678-687.	4.6	9
3	Fast Ionization-Front-Induced Anomalous Switching Behavior in Trigger Bipolar Transistors of Marx-Bank Circuits Under Base-Drive Conditions. IEEE Transactions on Plasma Science, 2018, 46, 2064-2071.	1.3	1
4	Circuit modeling and performance analysis of photoconductive antenna. Optics Communications, 2017, 394, 69-79.	2.1	25
5	2-D Analytical Modeling of Surface Potential and Threshold Voltage for Vertical Super-Thin Body FET. IEEE Transactions on Electron Devices, 2017, 64, 2106-2112.	3.0	11
6	SPICE level implementation of physics of filamentation in ESD protection devices. Microelectronics Reliability, 2017, 79, 239-247.	1.7	1
7	Effect of near fields on radiation from photoconductive antenna. , 2017, , .		2
8	Equivalent electrical circuit model of Terahertz photoconductive antenna receiver in a pulsed system. , 2017, , .		0
9	A High Precision Lumped Parameter Model for Piezoelectric Energy Harvesters. IEEE Sensors Journal, 2017, 17, 8350-8355.	4.7	15
10	Modeling Erratic Behavior Due to High Current Filamentation in Bipolar Structures Under Dynamic Avalanche Conditions. IEEE Transactions on Electron Devices, 2016, , 1-8.	3.0	4
11	A Novel Co-design Methodology for Optimizing ESD Protection Device Using Layout Level Approach. , 2016, , .		1
12	A Novel Capacitorless DRAM Cell Design Using Band-Gap Engineered Junctionless Double-Gate FET. , 2016, , .		0
13	Time dependent capacitance in Photoconductive Antenna. , 2015, , .		4
14	Design of band-gap engineered silicon-germanium Junctionless Double-gate FET for ZRAM application. , 2015, , .		1
15	A parametric study of photo carrier generation in Photoconductive Antenna. , 2015, , .		0
16	Methodology for optimizing ESD protection for high speed LVDS based I/Os. , 2015, , .		1
17	An Insight Into the ESD Behavior of the Nanometer-Scale Drain-Extended NMOS Deviceâ€™Part I: Turn-On Behavior of the Parasitic Bipolar. IEEE Transactions on Electron Devices, 2011, 58, 309-317.	3.0	12
18	An Insight Into ESD Behavior of Nanometer-Scale Drain Extended NMOS (DeNMOS) Devices: Part II (Two-Dimensional Study-Biasing & Comparison With NMOS). IEEE Transactions on Electron Devices, 2011, 58, 318-326.	3.0	7

#	ARTICLE	IF	CITATIONS
19	An Insight into the High Current ESD Behavior of Drain Extended NMOS (DENMOS) Devices in Nanometer Scale CMOS Technologies. , 2007, , .		8
20	Mechanisms leading to erratic snapback behavior in bipolar junction transistors with base emitter shorted. Journal of Applied Physics, 2005, 97, 084504.	2.5	10
21	Breakdown of transistors in Marx bank circuit. Solid-State Electronics, 2000, 44, 1679-1684.	1.4	11
22	The principle of operation of the avalanche transistor-based Marx bank circuit: A new perspective. Review of Scientific Instruments, 1998, 69, 2166-2170.	1.3	21