Isuru Nawinne

List of Publications by Year in descending order

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Version: 2024-02-01

3311381 2917675 6 9 1 2 citations h-index g-index papers 6 6 6 13 docs citations citing authors all docs times ranked

#	Article	IF	CITATIONS
1	Switchable cache: utilising dark silicon for application specific cache optimisations. IET Computers and Digital Techniques, 2016, 10, 157-164.	1.2	1
2	Exploring Multilevel Cache Hierarchies in Application Specific MPSoCs. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2015, 34, 1991-2003.	2.7	2
3	Hardware-based fast exploration of cache hierarchies in application specific MPSoCs., 2014,,.		1
4	Hardware-based fast exploration of cache hierarchies in application specific MPSoCs., 2014,,.		2
5	Latency-constrained binding of data flow graphs to energy conscious GALS-based MPSoCs., 2013,,.		2
6	A survey on exact cache design space exploration methodologies for application specific SoC memory hierarchies. , 2013 , , .		1