

# Rekha K James

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Enhancement and Modeling of Drain Current in Negative Capacitance Double Gate TFET. Silicon, 2022, 14, 6157-6167.	3.3	3
2	Interleaved Edge Routing in Buffered 3D Mesh & CMesh NoC. , 2021, , .		0
3	Dual Stage Encoding Technique to Minimize Cross Coupling across NoC Links. , 2021, , .		1
4	An improved tunnel field-effect transistor with an L-shaped gate and channel. Journal of Computational Electronics, 2020, 19, 304-309.	2.5	4
5	DoLaR: Double Layer Routing for Bufferless Mesh Network-on-Chip. , 2019, , .		2
6	Traffic Aware Deflection Rerouting Mechanism for Mesh Network on Chip. , 2018, , .		3
7	ReDC: Reduced Deflection CHIPPER Router for Bufferless NoCs. , 2018, , .		3
8	A GA Based Simple and Efficient Technique to Design Combinational Logic Circuits Using Universal Logic Modules. Journal of Circuits, Systems and Computers, 2016, 25, 1650074.	1.5	2
9	Genetic Algorithm Based Design of Combinational Logic Circuits Using Universal Logic Modules. Procedia Computer Science, 2015, 46, 1246-1253.	2.0	5
10	Optimal design of combinational logic circuits using genetic algorithm and Reed-Muller Universal Logic Modules. , 2014, , .		5
11	An improved design of combinational digital circuits with multiplexers using genetic algorithm. , 2013, , .		4