

Vivek Parmar

List of Publications by Year in descending order

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Version: 2024-02-01

25
papers

1,363
citations

1684188

5
h-index

1474206

9
g-index

25
all docs

25
docs citations

25
times ranked

3438
citing authors

#	ARTICLE	IF	CITATIONS
1	SLIM: Simultaneous Logic-in-Memory Computing Exploiting Bilayer Analog OxRAM Devices. Scientific Reports, 2020, 10, 2567.	3.3	1,186
2	Exploiting Intrinsic Variability of Filamentary Resistive Memory for Extreme Learning Machine Architectures. IEEE Nanotechnology Magazine, 2015, 14, 963-968.	2.0	26
3	OxRAM RNG Circuits Exploiting Multiple Undesirable Nanoscale Phenomena. IEEE Nanotechnology Magazine, 2017, 16, 560-566.	2.0	20
4	NV-BNN. , 2019, , .		20
5	Neuromorphic hybrid RRAM-CMOS RBM architecture. , 2015, , .		16
6	OXRAM based ELM architecture for multi-class classification applications. , 2015, , .		14
7	Dual-configuration in-memory computing bitcells using SiO _x RRAM for binary neural networks. Applied Physics Letters, 2022, 120, .	3.3	12
8	Neuromorphic Hardware Accelerated Adaptive Authentication System. , 2015, , .		10
9	Hybrid CMOS-OxRAM RNG circuits. , 2016, , .		9
10	Contrasting Advantages of Learning With Random Weights and Backpropagation in Non-Volatile Memory Neural Networks. IEEE Access, 2019, 7, 73938-73953.	4.2	8
11	Design Exploration of IoT centric Neural Inference Accelerators. , 2018, , .		7
12	Performance Enhancement of Edge-AI-Inference Using Commodity MRAM: IoT Case Study. , 2019, , .		7
13	Low-Power Hardware-Based Deep-Learning Diagnostics Support Case Study. , 2018, , .		5
14	Short-term plasticity circuit device exploration in the MASTISK neuromorphic framework. Journal Physics D: Applied Physics, 2018, 51, 454004.	2.8	4
15	Methodology for Realizing VMM with Binary RRAM Arrays: Experimental Demonstration of Binarized-ADALINE using OxRAM Crossbar. , 2020, , .		4
16	Hardware-Efficient Stochastic Binary CNN Architectures for Near-Sensor Computing. Frontiers in Neuroscience, 2021, 15, 781786.	2.8	4
17	An energy-efficient hybrid (CMOS-MTJ) TCAM using stochastic writes for approximate computing. , 2016, , .		3
18	Hyperspectral Image Classification for Remote Sensing Using Low-Power Neuromorphic Hardware. , 2019, , .		2

#	ARTICLE	IF	CITATIONS
19	MRAM-based BER resilient Quantized edge-AI Networks for Harsh Industrial Conditions. , 2021, , .		2
20	Exploiting Variability in Resistive Memory Devices for Cognitive Systems. Cognitive Systems Monographs, 2017, , 175-195.	0.1	2
21	MASTISK: Simulation Framework For Design Exploration Of Neuromorphic Hardware. , 2018, , .		1
22	A Hybrid CMOS-Memristive Approach to Designing Deep Generative Models. IEEE Transactions on Neural Networks and Learning Systems, 2021, 32, 2790-2796.	11.3	1
23	Investigation of Unified Emerging-NVM SoC Architecture for IoT-WSN Applications. , 2019, , .		0
24	Design of an Optimized CMOS ELM Accelerator. , 2019, , .		0
25	In-Memory Computation Based Mapping of Keccak-f Hash Function. Frontiers in Nanotechnology, 2022, 4, .	4.8	0