

Jangwoo Kim

List of Publications by Year in descending order

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11
papers

481
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1937685

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295
citing authors

#	ARTICLE	IF	CITATIONS
1	Building Fast, Dense, Low-Power Caches Using Erasure-Based Inline Multi-bit ECC. , 2013, , .		4
2	Multi-bit Error Tolerant Caches Using Two-Dimensional Error Coding. , 2007, , .		139
3	PAI: A Lightweight Mechanism for Single-Node Memory Recovery in DSM Servers. , 2007, , .		5
4	Multi-bit Error Tolerant Caches Using Two-Dimensional Error Coding. Microarchitecture (MICRO), Proceedings of the Annual International Symposium on, 2007, , .	0.0	9
5	Temporal Streaming of Shared Memory. Computer Architecture News, 2005, 33, 222-233.	2.5	35
6	Store-ordered streaming of shared memory. , 2005, , .		11
7	Truss: A Reliable, Scalable Server Architecture. IEEE Micro, 2005, 25, 51-59.	1.8	17
8	SimFlex. Performance Evaluation Review, 2004, 31, 31-34.	0.6	116
9	Efficient Resource Sharing in Concurrent Error Detecting Superscalar Microarchitectures. , 0, , .		61
10	Temporal Streaming of Shared Memory. , 0, , .		83
11	Understanding the performance of concurrent error detecting superscalar microarchitectures. , 0, , .		1