Jiang Lin

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/11299487/publications.pdf

Version: 2024-02-01

		2682572	2550090	
6	365	2	3	
papers	citations	h-index	g-index	
6	6	6	203	
all docs	docs citations	times ranked	citing authors	

#	Article	IF	CITATIONS
1	Mini-rank: Adaptive DRAM architecture for improving memory power efficiency. , 2008, , .		188
2	Gaining insights into multicore cache partitioning: Bridging the gap between simulation and real systems. High Performance Computer Architecture (HPCA), Proceedings of the IEEE International Symposium on, 2008, , .	0.0	76
3	Soft-OLP: Improving Hardware Cache Performance through Software-Controlled Object-Level Partitioning. , 2009, , .		47
4	Thermal modeling and management of DRAM memory systems. , 2007, , .		45
5	Software thermal management of dram memory for multicore systems. Performance Evaluation Review, 2008, 36, 337-348.	0.6	7
6	Decoupled DIMM. Computer Architecture News, 2009, 37, 255-266.	2.5	2