

# Jing Pu

## List of Publications by Year in descending order

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23  
papers

235  
citations

1163117

8  
h-index

1058476

14  
g-index

23  
all docs

23  
docs citations

23  
times ranked

416  
citing authors

#	ARTICLE	IF	CITATIONS
1	Hybrid integrated single-wavelength laser with silicon micro-ring reflector. , 2018, , .		1
2	Ultrahigh Index and Low-loss Silicon Rich Nitride Thin film for NIR HAMR Optics. IEEE Transactions on Magnetics, 2017, 53, 1-7.	2.1	7
3	Comparison of III-V/Si on-chip lasers with etched facet reflectors. Applied Optics, 2017, 56, 5086.	2.1	1
4	Generic heterogeneously integrated III-V lasers-on-chip with metal-coated etched-mirror. Proceedings of SPIE, 2016, , .	0.8	0
5	Generic Heterogeneously Integrated III-V Lasers-on-Chip With Metal-Coated Etched-Mirror. IEEE Journal of Selected Topics in Quantum Electronics, 2016, 22, 7-15.	2.9	3
6	A novel design on ultra-thin lateral-current-injection III-V on silicon with ultra-compact optical VIA for heterogeneous electronic-photonic integration. Optical and Quantum Electronics, 2016, 48, 1.	3.3	2
7	Design, fabrication and demonstration of heterogeneously III-V/Si laser with a compact optical vertical interconnect access. , 2015, , .		0
8	Heterogeneous III-V/Si photonic integration: Configuration and optical coupling. , 2015, , .		0
9	Heterogeneously integrated III-V laser on thin SOI with compact optical vertical interconnect access. Optics Letters, 2015, 40, 1378.	3.3	19
10	Heterogeneous Integrated III-V Laser on Thin SOI With Single-Stage Adiabatic Coupler: Device Realization and Performance Analysis. IEEE Journal of Selected Topics in Quantum Electronics, 2015, 21, 369-376.	2.9	13
11	Thin-silicon-film platform for single-mode lasers. , 2014, , .		0
12	Optical Design of Distributed Feedback Lasers-on-Thin-Film-Silicon. IEEE Photonics Technology Letters, 2013, 25, 944-947.	2.5	3
13	Fabrication of low-loss silicon nanophotonic waveguide for photonic device integration. , 2013, , .		0
14	Fabrication of high-efficiency heterogeneous Si/III-V integration with short optical vertical interconnect access. Proceedings of SPIE, 2013, , .	0.8	0
15	Demonstration of heterogeneous III-V/Si integration with a compact optical vertical interconnect access. Optics Letters, 2013, 38, 5353.	3.3	18
16	A novel type of ultra-compact lateral-current-injection III/V photonic device integrated on SOI for electronic-photonic chip application. , 2013, , .		0
17	Heterogeneous Si/III-V integration and the optical vertical interconnect access. Optics Express, 2012, 20, 16745.	3.4	25
18	Low Temperature Direct Bonding of InP and Si <sub>3</sub> N <sub>4</sub> -Coated Silicon Wafers for Photonic Device Integration. Journal of the Electrochemical Society, 2012, 159, H507-H510.	2.9	16

#	ARTICLE	IF	CITATIONS
19	Wide memory window in graphene oxide charge storage nodes. Applied Physics Letters, 2010, 96, .	3.3	87
20	Performance Improvement in Charge-Trap Flash Memory Using Lanthanum-Based High- $\kappa$ Blocking Oxide. IEEE Transactions on Electron Devices, 2009, 56, 2746-2751.	3.0	26
21	Carbon-Doped Polysilicon Floating Gate for Improved Data Retention and P/E Window of Flash Memory. IEEE Electron Device Letters, 2008, 29, 688-690.	3.9	1
22	A novel floating gate engineering technique for improved data retention of flash memory devices. , 2008, , .		0
23	P-Type Floating Gate for Retention and P/E Window Improvement of Flash Memory Devices. IEEE Transactions on Electron Devices, 2007, 54, 1910-1917.	3.0	13