

Miaoqing Huang

List of Publications by Year in descending order

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45
papers

477
citations

1163117

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12
g-index

45
all docs

45
docs citations

45
times ranked

388
citing authors

#	ARTICLE	IF	CITATIONS
1	The Promise of High-Performance Reconfigurable Computing. Computer, 2008, 41, 69-76.	1.1	126
2	New Hardware Architectures for Montgomery Modular Multiplication Algorithm. IEEE Transactions on Computers, 2011, 60, 923-936.	3.4	82
3	A hybrid parallel cellular automata model for urban growth simulation over GPU/CPU heterogeneous architectures. International Journal of Geographical Information Science, 2016, 30, 494-514.	4.8	50
4	Exploiting concurrent kernel execution on graphic processing units. , 2011, , .		34
5	Modular Design of Fully Pipelined Reduction Circuits on FPGAs. IEEE Transactions on Parallel and Distributed Systems, 2013, 24, 1818-1826.	5.6	16
6	Geocomputation over the Emerging Heterogeneous Computing Infrastructure. Transactions in GIS, 2014, 18, 3-24.	2.3	15
7	Scaling scientific applications on clusters of hybrid multicore/GPU nodes. , 2011, , .		14
8	Efficient Mapping of Task Graphs onto Reconfigurable Hardware Using Architectural Variants. IEEE Transactions on Computers, 2012, 61, 1354-1360.	3.4	12
9	Unsupervised image classification over supercomputers Kraken, Keeneland and Beacon. GIScience and Remote Sensing, 2014, 51, 321-338.	5.9	10
10	A unified OpenCL-flavor programming model with scalable hybrid hardware platform on FPGAs. , 2014, , .		9
11	Accelerating Mean Shift Segmentation Algorithm on Hybrid CPU/GPU Platforms. , 2013, , 157-166.		9
12	Portable library development for reconfigurable computing systems: A case study. Parallel Computing, 2008, 34, 245-260.	2.1	8
13	Hardware task scheduling optimizations for reconfigurable computing. , 2008, , .		8
14	Developing application-specific multiprocessor platforms on FPGAs. , 2012, , .		8
15	RDMS: A hardware task scheduling algorithm for Reconfigurable Computing. , 2009, , .		7
16	Efficient Mapping of Hardware Tasks on Reconfigurable Computers Using Libraries of Architecture Variants. , 2009, , .		6
17	A Delay-based PUF Design Using Multiplexers on FPGA. , 2013, , .		6
18	Creating HW/SW co-designed MPSoPC's from high level programming models. , 2011, , .		5

#	ARTICLE	IF	CITATIONS
19	High-Performance Reconfigurable Computing. International Journal of Reconfigurable Computing, 2012, 2012, 1-2.	0.2	5
20	Archborn: an open source tool for automated generation of chip heterogeneous multiprocessor architectures. , 2015, , .		5
21	Modelling the Interaction of THz Waves with Breast Cancer Tissues. , 2018, , .		5
22	Reaping the Processing Potential of FPGA on Double-Precision Floating-Point Operations: An Eigenvalue Solver Case Study. , 2010, , .		4
23	Towards efficient GPU sharing on multicore processors. Performance Evaluation Review, 2012, 40, 119-124.	0.6	4
24	A delay-based PUF design using multiplexer chains. , 2013, , .		4
25	Accelerating Applications Using GPUs on Embedded Systems and Mobile Devices. , 2013, , .		3
26	Improve Effective Capacity and Lifetime of Solid State Drives. , 2013, , .		3
27	Improve memory access for achieving both performance and energy efficiencies on heterogeneous systems. , 2014, , .		3
28	Efficient utilization of multi-core processors and many-core co-processors on supercomputer beacon for scalable geocomputation and geo-simulation over big earth data. Big Earth Data, 2018, 2, 65-85.	4.4	3
29	Parameterized Hardware Design on Reconfigurable Computers: An Image Processing Case Study. International Journal of Reconfigurable Computing, 2010, 2010, 1-11.	0.2	2
30	Modular design of fully pipelined accumulators. , 2010, , .		2
31	Parallelizing computer vision algorithms on acceleration technologies: A SIFT case study. , 2014, , .		2
32	OGen: An Automated Generation Tool for Custom MPSoC Architectures Based on Object-Oriented Programming Methods. , 2016, , .		2
33	A Portable Memory Access Framework on Reconfigurable Computers. , 2007, , .		1
34	Improving the Performance of On-Board Cache for Flash-Based Solid-State Drives. , 2012, , .		1
35	A Hierarchical Memory Architecture with NoC Support for MPSoC on FPGAs. , 2014, , .		1
36	Towards Optimal Task Distribution on Computer Clusters with Intel MIC Coprocessors. , 2015, , .		1

#	ARTICLE	IF	CITATIONS
37	PolyPC: Polymorphic parallel computing framework on embedded reconfigurable system. , 2017, , .		1
38	Achieving energy-efficiency on MPSoCs: performance and power optimizations. , 2015, , .		0
39	Exploiting hardware abstraction for hybrid parallel computing framework. , 2015, , .		0
40	Performance and Energy Optimization on MPSoCs by Enabling STT-MRAM LUTs. , 2015, , .		0
41	Cyberinfrastructure and High-Performance Computing. , 2018, , 341-354.		0
42	Design and Demonstration of a SIMD System Based on the Bit-serial PE Array in FPGA. , 2019, , .		0
43	GPGPU in GIS. , 2016, , 1-8.		0
44	MIC in GIS. , 2016, , 1-6.		0
45	MIC in GIS. , 2017, , 1226-1232.		0