

Vivek Gopinathan

List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Advanced hybrid SVPWM techniques for two level VSI. International Journal of Electronics, 2021, 108, 1439-1464.	1.4	0
2	Design space exploration of optimized hybrid SVPWM techniques based on spatial region for three level VSI. Electrical Engineering, 2021, 103, 2473-2495.	2.0	3
3	A Synchronised Hybrid SVPWM Technique Based on MDAVT Switching Sequence for Reduced THD. , 2019, , .		1
4	Simplified double switching SVPWM implementation for threeâ€level VSI. Journal of Engineering, 2019, 2019, 8257-8269.	1.1	1
5	Optimised Design and Implementation of SVPWM Switching sequences for Five level VSI. , 2018, , .		3
6	An Optimum Hybrid SVPWM Technique for Three Level Inverter. , 2018, , .		0
7	Common Mode Voltage (CMV) in Three level NPC VSI using Advanced Bus clamping methods:A Study. , 2018, , .		2
8	An Optimized Hybrid SVPWM Strategy Based on Multiple Division of Active Vector Time (MDAVT). IEEE Transactions on Power Electronics, 2017, 32, 4607-4618.	7.9	36
9	Experimental study on SVPWM switching sequences for VSIs. , 2016, , .		3
10	Synchronised Microcontroller Architecture to eliminate dominant harmonics from Quasi square waveform. , 2016, , .		0
11	Online reduction of fifth and seventh harmonics in single phase quasi square wave inverters. , 2015, , .		0
12	Performance study of advanced discontinuous SVPWM methods with zero changing angle variation. , 2015, , .		4
13	Performance evaluation of clamping position variation on advanced bus clamping strategies: Experimental investigation. , 2015, , .		6
14	A comparative investigation of various advanced bus clamped space vector pulse width modulation (SVPWM) techniques. , 2014, , .		13