Nimrod Wald

List of Publications by Year
in descending order

17	1,568	1307594 7	1588992 8
papers	citations	h-index	g-index

17	17	17	1001
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	mMPUâ€"A Real Processing-in-Memory Architecture to Combat the von Neumann Bottleneck. Springer Series in Advanced Microelectronics, 2020, , 191-213.	0.3	20
2	Memristor-based in-memory logic and its application in image processing. , 2020, , 175-194.		0
3	Understanding the influence of device, circuit and environmental variations on real processing in memristive memory using Memristor Aided Logic. Microelectronics Journal, 2019, 86, 22-33.	2.0	21
4	Two-terminal floating-gate transistors with a low-power memristive operation mode for analogue neuromorphic computing. Nature Electronics, 2019, 2, 596-605.	26.0	88
5	A Taxonomy and Evaluation Framework for Memristive Logic. , 2019, , 1065-1099.		7
6	Not in Name Alone: A Memristive Memory Processing Unit for Real In-Memory Processing. IEEE Micro, 2018, 38, 13-21.	1.8	29
7	IMAGING: In-Memory AlGorithms for Image processiNG. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 4258-4271.	5.4	39
8	Practical challenges in delivering the promises of real processing-in-memory machines. , 2018, , .		20
9	Efficient Algorithms for In-Memory Fixed Point Multiplication Using MAGIC. , 2018, , .		51
10	Memristor for computing: Myth or reality?. , 2017, , .		79
11	Simple magic: Synthesis and in-memory Mapping of logic execution for memristor-aided logic. , 2017, , .		32
12	Memristive logic: A framework for evaluation and comparison. , 2017, , .		62
13	Logic with Unipolar Memristors â€" Circuits and Design Methodology. IFIP Advances in Information and Communication Technology, 2017, , 24-40.	0.7	4
14	Design methodology for stateful memristive logic gates. , 2016, , .		10
15	MAGICâ€"Memristor-Aided Logic. IEEE Transactions on Circuits and Systems II: Express Briefs, 2014, 61, 895-899.	3.0	542
16	Memristor-Based Material Implication (IMPLY) Logic: Design Principles and Methodologies. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2054-2066.	3.1	453
17	MRL — Memristor Ratioed Logic. , 2012, , .		111