

Shiromani Balmukund Rahi

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	Low Power Circuit and System Design Hierarchy and Thermal Reliability of Tunnel Field Effect Transistor. Silicon, 2022, 14, 3233-3243.	3.3	7
2	Analytical Compact Model of Nanowire Junctionless Gate-All-Around MOSFET Implemented in Verilog-A for Circuit Simulation. Silicon, 2022, 14, 10967-10976.	3.3	4
3	Investigation of Analog Parameters and Miller Capacitance Affecting the Circuit Performance of Double Gate Tunnel Field Effect Transistors. Advances in Intelligent Systems and Computing, 2021, , 335-349.	0.6	1
4	A review on emerging negative capacitance field effect transistor for low power electronics. Microelectronics Journal, 2021, 116, 105242.	2.0	32
5	Heterogate junctionless tunnel field-effect transistor: future of low-power devices. Journal of Computational Electronics, 2017, 16, 30-38.	2.5	47
6	High-k double gate junctionless tunnel FET with a tunable bandgap. RSC Advances, 2015, 5, 54544-54550.	3.6	28
7	Compact analytical model of double gate junction-less field effect transistor comprising quantum-mechanical effect. Journal of Semiconductors, 2015, 36, 024001.	3.7	12
8	Improved performance of a junctionless tunnel field effect transistor with a Si and SiGe heterostructure for ultra low power applications. RSC Advances, 2015, 5, 48779-48785.	3.6	26
9	Temperature effect on hetero structure junctionless tunnel FET. Journal of Semiconductors, 2015, 36, 034002.	3.7	28
10	A simulation-based proposed high- k heterostructure AlGaAs/Si junctionless n-type tunnel FET. Journal of Semiconductors, 2014, 35, 114005.	3.7	22