

Vishva Ray

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11181518/publications.pdf>

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14
papers

2,154
citations

840776

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1125743

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docs citations

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times ranked

3334
citing authors

#	ARTICLE	IF	CITATIONS
1	Plasmonic Metasurfaces with High UV-Vis Transmittance for Photopatterning of Designer Molecular Orientations. <i>Advanced Optical Materials</i> , 2019, 7, 1900117.	7.3	17
2	Linear polarization distinguishing metalens in visible wavelength. <i>Optics Letters</i> , 2019, 44, 399.	3.3	6
3	Continuous achromatic flat subwavelength grating lens over whole visible bandwidths. , 2019, , .		0
4	Achromatic Flat Subwavelength Grating Lens Over Whole Visible Bandwidths. <i>IEEE Photonics Technology Letters</i> , 2018, 30, 955-958.	2.5	22
5	Polarization rotation with ultra-thin bianisotropic metasurfaces. <i>Optica</i> , 2016, 3, 427.	9.3	74
6	Breaking Malus's law: Highly efficient, broadband, and angular robust asymmetric light transmitting metasurface. <i>Laser and Photonics Reviews</i> , 2016, 10, 791-798.	8.7	38
7	Energy-filtered cold electron transport at room temperature. <i>Nature Communications</i> , 2014, 5, 4745.	12.8	23
8	High Performance Bianisotropic Metasurfaces: Asymmetric Transmission of Light. <i>Physical Review Letters</i> , 2014, 113, 023902.	7.8	317
9	Nanoimprinted substrates for high-yield production of topological insulator nanoribbons. <i>Applied Physics A: Materials Science and Processing</i> , 2013, 111, 755-766.	2.3	4
10	Rapid electronic detection of probe-specific microRNAs using thin nanopore sensors. <i>Nature Nanotechnology</i> , 2010, 5, 807-814.	31.5	632
11	DNA Translocation through Graphene Nanopores. <i>Nano Letters</i> , 2010, 10, 2915-2921.	9.1	846
12	CMOS-compatible fabrication of room-temperature single-electron devices. <i>Nature Nanotechnology</i> , 2008, 3, 603-608.	31.5	81
13	Single-particle placement via self-limiting electrostatic gating. <i>Applied Physics Letters</i> , 2008, 93, 073110.	3.3	14
14	Electrostatic Funneling for Precise Nanoparticle Placement: A Route to Wafer-Scale Integration. <i>Nano Letters</i> , 2007, 7, 439-445.	9.1	80