

Prashanth Kumar

List of Publications by Year in descending order

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papers

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60
citing authors

#	ARTICLE	IF	CITATIONS
1	Analysis and Simulation of Schottky Tunneling Using Schottky Barrier FET with 2-D Analytical Modeling. Silicon, 2022, 14, 831-837.	3.3	10
2	Performance Analysis of Double Gate Dielectric Modulation in Schottky FET as Biomolecule Sensor. Silicon, 2022, 14, 4767-4773.	3.3	5
3	Source-Drain Junction Engineering Schottky Barrier MOSFETs and their Mixed Mode Application. Silicon, 2020, 12, 821-830.	3.3	22
4	Impact of ferroelectric on the electrical characteristics of silicon-germanium based heterojunction Schottky barrier FET. AEU - International Journal of Electronics and Communications, 2019, 107, 257-263.	2.9	21
5	A physics-based threshold voltage model for heterodielectric dual material gate Schottky barrier MOSFET. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2018, 31, e2320.	1.9	19
6	Scaling of Dopant Segregation Schottky Barrier Using Metal Strip Buried Oxide MOSFET and its Comparison with Conventional Device. Silicon, 2018, 10, 811-820.	3.3	22
7	Suppression of ambipolar conduction and investigation of RF performance characteristics of gate-drain underlap SiGe Schottky barrier field effect transistor. Micro and Nano Letters, 2018, 13, 626-630.	1.3	23
8	2-D analytical modeling for electrostatic potential and threshold voltage of a dual work function gate Schottky barrier MOSFET. Journal of Computational Electronics, 2017, 16, 658-665.	2.5	20
9	2D analytical model for surface potential based electric field and impact of work function in DMG SB MOSFET. Superlattices and Microstructures, 2017, 109, 805-814.	3.1	20
10	Recent Study on Schottky Tunnel Field Effect Transistor for Biosensing Applications. Silicon, 0, , .	3.3	2