

Si-Nai Kim

List of Publications by Year in descending order

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citing authors

#	ARTICLE	IF	CITATIONS
1	A 65-nm CMOS 6-Bit 20 GS/s Time-Interleaved DAC With Full-Binary Sub-DACs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1154-1158.	3.0	9
2	A SUC-Based Full-Binary 6-bit 3.1-GS/s 17.7-mW Current-Steering DAC in 0.038 mm ² . IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 794-798.	3.1	17