

# Michael Wu

## List of Publications by Year in descending order

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21  
papers

914  
citations

1937685

4  
h-index

2272923

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21  
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21  
docs citations

21  
times ranked

579  
citing authors

#	ARTICLE	IF	CITATIONS
1	Implicit vs. Explicit Approximate Matrix Inversion for Wideband Massive MU-MIMO Data Detection. Journal of Signal Processing Systems, 2018, 90, 1311-1328.	2.1	14
2	FPGA design of a coordinate descent data detector for large-scale MU-MIMO. , 2016, , .		23
3	High-Throughput Data Detection for Massive MU-MIMO-OFDM Using Coordinate Descent. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 2357-2367.	5.4	69
4	Linear large-scale MIMO data detection for 5G multi-carrier waveform candidates. , 2015, , .		13
5	Accelerating massive MIMO uplink detection on GPU for SDR systems. , 2015, , .		12
6	VLSI design of large-scale soft-output MIMO detection using conjugate gradients. , 2015, , .		48
7	Conjugate gradient-based soft-output detection and precoding in massive MIMO systems. , 2014, , .		118
8	A high performance GPU-based software-defined basestation. , 2014, , .		11
9	Large-Scale MIMO Detection for 3GPP LTE: Algorithms and FPGA Implementations. IEEE Journal on Selected Topics in Signal Processing, 2014, 8, 916-929.	10.8	329
10	A 3.8Gb/s large-scale MIMO detector for 3GPP LTE-Advanced. , 2014, , .		27
11	A fast and efficient sift detector using the mobile GPU. , 2013, , .		55
12	High throughput low latency LDPC decoding on GPU for SDR systems. , 2013, , .		49
13	Parallel nonbinary LDPC decoding on GPU. , 2012, , .		13
14	Flexible N-Way MIMO Detector on GPU. , 2012, , .		4
15	Low complexity scalable MIMO sphere detection through antenna detection reordering. Analog Integrated Circuits and Signal Processing, 2012, 73, 463-472.	1.4	0
16	GPU accelerated scalable parallel decoding of LDPC codes. , 2011, , .		15
17	Implementation of a High Throughput Soft MIMO Detector on GPU. Journal of Signal Processing Systems, 2011, 64, 123-136.	2.1	36
18	Implementation of a 3GPP LTE turbo decoder accelerator on GPU. , 2010, , .		29

#	ARTICLE	IF	CITATIONS
19	FPGA prototyping of a high data rate LTE uplink baseband receiver. , 2009, , .		10
20	A GPU implementation of a real-time MIMO detector. , 2009, , .		23
21	Reconfigurable real-time MIMO detector on GPU. , 2009, , .		16