

Bharathwaj Muthuswamy

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11131639/publications.pdf>

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11
papers

1,040
citations

2258059

3
h-index

2272923

4
g-index

13
all docs

13
docs citations

13
times ranked

682
citing authors

#	ARTICLE	IF	CITATIONS
1	IMPLEMENTING MEMRISTOR BASED CHAOTIC CIRCUITS. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2010, 20, 1335-1350.	1.7	515
2	SIMPLEST CHAOTIC CIRCUIT. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2010, 20, 1567-1580.	1.7	432
3	A Generic Model of Memristors With Parasitic Components. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 891-898.	5.4	70
4	Introduction to Nonlinear Circuits and Networks. , 2019, , .		8
5	A mixed-signal EEG interface circuit for use in first year electronics courses. , 2012, , .		5
6	OPTIMAL CNN TEMPLATES FOR LINEARLY-SEPARABLE ONE-DIMENSIONAL CELLULAR AUTOMATA. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2007, 17, 749-790.	1.7	3
7	From Van der Pol to Chua: An introduction to nonlinear dynamics and chaos for second year undergraduates. , 2012, , .		2
8	CHUA'S EQUATION WAS PROVED TO BE CHAOTIC IN TWO YEARS, LORENZ EQUATION IN THIRTY SIX YEARS. , 2013, , 324-336.		1
9	Chaotic ODEs: FPGA Examples. Emergence, Complexity and Computation, 2015, , 55-80.	0.3	0
10	Dynamic Nonlinear Networks. , 2019, , 199-314.		0
11	A Framework For Teaching Nonlinear Op Amp Circuits To Junior Undergraduate Electrical Engineering Students. , 0, , .		0