

# Kuk-Hwan Kim

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11131087/publications.pdf>

Version: 2024-02-01

12  
papers

2,408  
citations

1163117

8  
h-index

1474206

9  
g-index

12  
all docs

12  
docs citations

12  
times ranked

2750  
citing authors

#	ARTICLE	IF	CITATIONS
1	Crossbar RRAM Arrays: Selector Device Requirements During Read Operation. IEEE Transactions on Electron Devices, 2014, 61, 1369-1376.	3.0	180
2	A Functional Hybrid Memristor Crossbar-Array/CMOS System for Data Storage and Neuromorphic Applications. Nano Letters, 2012, 12, 389-395.	9.1	745
3	CMOS-integrated memristors for neuromorphic architectures. , 2011, , .		3
4	Device and SPICE modeling of RRAM devices. Nanoscale, 2011, 3, 3833.	5.6	84
5	Two-terminal resistive switches (memristors) for memory and logic applications. , 2011, , .		77
6	Synaptic behaviors and modeling of a metal oxide memristive device. Applied Physics A: Materials Science and Processing, 2011, 102, 857-863.	2.3	355
7	Nanoscale resistive memory with intrinsic diode characteristics and long endurance. Applied Physics Letters, 2010, 96, .	3.3	144
8	Si Memristive devices applied to memory and neuromorphic circuits. , 2010, , .		24
9	Radio-Frequency Operation of Transparent Nanowire Thin-Film Transistors. IEEE Electron Device Letters, 2009, 30, 730-732.	3.9	15
10	High-Density Crossbar Arrays Based on a Si Memristive System. Nano Letters, 2009, 9, 870-874.	9.1	507
11	Programmable Resistance Switching in Nanoscale Two-Terminal Devices. Nano Letters, 2009, 9, 496-500.	9.1	272
12	Doping-Free Nanoscale Complementary Carbon-Nanotube Field-Effect Transistors with DNA-Templated Molecular Lithography. Small, 2008, 4, 1959-1963.	10.0	2