

Rizwan Bashirullah

List of Publications by Year in descending order

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42
papers

787
citations

933447

10
h-index

996975

15
g-index

42
all docs

42
docs citations

42
times ranked

835
citing authors

#	ARTICLE	IF	CITATIONS
1	A multi-channel peripheral nerve stimulator with integrate-and-fire encoding. Journal of Medical Engineering and Technology, 2021, 45, 187-196.	1.4	0
2	Splenic Nerve Neuromodulation Reduces Inflammation and Promotes Resolution in Chronically Implanted Pigs. Frontiers in Immunology, 2021, 12, 649786.	4.8	17
3	A synthesizable time-based LDO using digital standard cells and analog pass transistor. , 2017, , .		9
4	Intraneural active probe for bidirectional peripheral nerve interface. , 2017, , .		11
5	A 4.7 T/11.1 T NMR Compliant 50 nW Wirelessly Programmable Implant for Bioartificial Pancreas <i>In Vivo</i> Monitoring. IEEE Journal of Solid-State Circuits, 2016, 51, 473-483.	5.4	5
6	Biofluid Activated Microbattery for Disposable Microsystems. Journal of Microelectromechanical Systems, 2015, 24, 70-79.	2.5	12
7	A 0.45V CMOS relaxation oscillator with ±2.5% frequency stability from −55°C to 125°C. , 2015, , .		5
8	Airgap Interconnects: Modeling, Optimization, and Benchmarking for Backplane, PCB, and Interposer Applications. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2014, 4, 1335-1346.	2.5	30
9	A Current-Density Centric Logical Effort Delay and Power Model for High-Speed CML Gates. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2618-2630.	5.4	9
10	A 12.4-mW 4.5-Gb/s Receiver With Majority-Voting 1-Tap Speculative DFE in 0.13- μm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 867-871.	3.0	8
11	High-Voltage Tolerant Digitally Aided DCM/PWM Multiphase DC-DC Boost Converter With Integrated Schottky Diodes in 0.13 μm 1.2 V Digital CMOS Process. IEEE Journal of Solid-State Circuits, 2013, 48, 774-789.	5.4	13
12	Design and fabrication of ultra low-loss, high-performance 3D chip-chip air-clad interconnect pathway. , 2013, , .		5
13	50–100 MHz, 8x step-up DC-DC converters in 130nm 1.2V digital CMOS. , 2011, , .		4
14	Nuclear magnetic resonance energy harvesting for ultra-low power biomedical implants. , 2011, , .		2
15	Towards miniature step-up power converters for mobile microsystems. , 2011, , .		4
16	Air cavity low-loss transmission lines for high speed serial link applications. , 2011, , .		4
17	Low power microsystems for brain computer interfaces. , 2011, , .		0
18	Modeling, optimization and benchmarking of chip-to-chip electrical interconnects with low loss air-clad dielectrics. , 2011, , .		9

#	ARTICLE	IF	CITATIONS
19	A 90°/240 MHz Hysteretic Controlled DC-DC Buck Converter With Digital Phase Locked Loop Synchronization. IEEE Journal of Solid-State Circuits, 2011, 46, 2108-2119.	5.4	121
20	Channel characterization for galvanic coupled in vivo biomedical devices. , 2011, , .		8
21	Wireless Implants. IEEE Microwave Magazine, 2010, 11, S14-S23.	0.8	146
22	Optimal data rates for electrical links and opportunities for 3D integration and photonics. , 2010, , .		1
23	An autonomous real-time neural signal processor. , 2009, , .		3
24	Air cavity low-loss signal lines on bt substrates for high frequency chip-to-chip communication. , 2009, , .		2
25	A Delay-Locked Loop Synchronization Scheme for High-Frequency Multiphase Hysteretic DC-DC Converters. IEEE Journal of Solid-State Circuits, 2009, 44, 3131-3145.	5.4	73
26	An adaptive neural spike detector with threshold-lock loop. , 2009, , .		7
27	Capsule antennas for medication compliance monitoring. , 2009, , .		3
28	An asymmetric RF tagging IC for ingestible medication compliance capsules. , 2009, , .		16
29	A 32-Gb/s On-Chip Bus With Driver Pre-Emphasis Signaling. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2009, 17, 1267-1274.	3.1	13
30	Toward Energy Efficient Neural Interfaces. IEEE Transactions on Biomedical Engineering, 2009, 56, 2697-2700.	4.2	12
31	Design of Multigigabit-per-Second Transceiver for Band-Limited High-Speed Data Communication Using DC-Free Signaling. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 1555-1564.	4.6	1
32	An active crosstalk reduction technique for parallel high-speed links in low cost wirebond BGA packages. , 2008, , .		0
33	A 90°/240MHz hysteretic controlled DC-DC buck converter with digital PLL frequency locking. , 2008, , .		6
34	A high-frequency DC-DC converter based power amplifier for direct RF carrier generation. , 2007, , .		0
35	Feasibility Study of Printed Capsule Antennas for Medication Compliance Monitoring. , 2007, , .		1
36	A DLL Based Multiphase Hysteretic DC-DC Converter. , 2007, , .		3

#	ARTICLE	IF	CITATIONS
37	A Delay Locked Loop Synchronization Scheme for High Frequency Multiphase Hysteretic DC-DC Converters. , 2007, , .		8
38	Voltage-Mode Driver Preemphasis Technique For On-Chip Global Buses. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2007, 15, 231-236.	3.1	9
39	A Wireless Power Interface for Rechargeable Battery Operated Medical Implants. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2007, 54, 912-916.	2.2	182
40	A Low Power ASK Clock and Data Recovery Circuit for Wireless Implantable Electronics. , 2006, , .		25
41	A 25-GHz CMOS Tunable Injection Locking Quadrature LC-Divider for Multi-Channel Transceivers. , 2006, , .		0
42	A Wireless Power Interface for Rechargeable Battery Operated Neural Recording Implants. Annual International Conference of the IEEE Engineering in Medicine and Biology Society, 2006, , .	0.5	0