

Samir Ahmad Mussa

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/11109446/publications.pdf>

Version: 2024-02-01

31
papers

692
citations

1040056

9
h-index

1199594

12
g-index

31
all docs

31
docs citations

31
times ranked

704
citing authors

#	ARTICLE	IF	CITATIONS
1	GaN-based single-phase bridgeless PFC boost rectifier. Journal of Engineering, 2019, 2019, 3614-3617.	1.1	2
2	Symmetrical Hybrid Multilevel VSI and CSI Inverters Derived from Dc-Dc Converters. , 2019, , .		0
3	High-resolution FPGA-pulse width modulation applied to PFC 2MHz converter using eGaN field effect transistor. Journal of Engineering, 2019, 2019, 4205-4208.	1.1	0
4	Evaluation of SiC-Based Three-Phase PFC Rectifier Design. , 2018, , .		1
5	Single-phase hybrid boost rectifiers with high voltage gain and high power factor. , 2016, , .		0
6	Design of a high performance ac-ac drive system based on indirect matrix converters employing SiC MOSFETs. , 2015, , .		0
7	Three-Phase Multilevel PFC Rectifier Based on Multistate Switching Cells. IEEE Transactions on Power Electronics, 2015, 30, 1843-1854.	7.9	31
8	Control Strategy for Current Harmonic Programmed AC Active Electronic Power Loads. IEEE Transactions on Industrial Electronics, 2014, 61, 3810-3822.	7.9	20
9	Multilevel Buck/Boost-Type DC-DC Converter for High-Power and High-Voltage Application. IEEE Transactions on Industry Applications, 2014, 50, 3931-3942.	4.9	57
10	Active Power Filter Control Strategy With Implicit Closed-Loop Current Control and Resonant Controller. IEEE Transactions on Industrial Electronics, 2013, 60, 2721-2730.	7.9	130
11	Generalized Analysis of a Multistate Switching Cells-Based Single-Phase Multilevel PFC Rectifier. IEEE Transactions on Power Electronics, 2012, 27, 46-56.	7.9	17
12	Modified hybrid symmetrical multilevel inverter. , 2012, , .		2
13	Three-Phase Hybrid Multilevel Inverter Based on Half-Bridge Modules. IEEE Transactions on Industrial Electronics, 2012, 59, 668-678.	7.9	100
14	Current technique applied in single phase PFC boost converter based on discrete-time One Cycle Control. , 2011, , .		3
15	Discrete-time one cycle control technique applied in single-phase PFC boost converter. , 2011, , .		2
16	Analysis of indirect matrix converter topologies in voltage step-up operation mode. , 2011, , .		7
17	Lyapunov based stability analysis of current self-control approach for single-phase PFC Boost converters subject to load variations. , 2011, , .		2
18	Three-Phase Multilevel PWM Rectifiers Based on Conventional Bidirectional Converters. IEEE Transactions on Power Electronics, 2010, 25, 545-549.	7.9	71

#	ARTICLE	IF	CITATIONS
19	Cascaded Symmetrical Hybrid Multilevel Dc-Ac converter. , 2010, , .		11
20	Bidirectional rectifier — Comparison between average current and “dq0” coordinates control techniques implemented on DSP. , 2010, , .		1
21	Synchronous state machine inner FPGA controlling PFC boost converter. , 2010, , .		5
22	Symmetrical Hybrid Multilevel DC—AC Converters With Reduced Number of Insulated DC Supplies. IEEE Transactions on Industrial Electronics, 2010, 57, 2307-2314.	7.9	141
23	Current control techniques applied in PFC boost converter at instantaneous power interruption. , 2009, , .		5
24	Discrete-time current control techniques applied in PFC boost converter at instantaneous power interruption. , 2009, , .		5
25	Three-Level ZVS Active Clamping PWM for the DC—DC Buck Converter. IEEE Transactions on Power Electronics, 2009, 24, 2249-2258.	7.9	60
26	Single-phase PFC boost converter operating at instantaneous power interruption. , 2009, , .		2
27	New symmetrical hybrid multilevel DC-AC converters. Power Electronics Specialist Conference (PESC), IEEE, 2008, , .	0.0	7
28	Digital control based on DPLL of an AC line conditioner. Power Electronics Specialist Conference (PESC), IEEE, 2008, , .	0.0	0
29	Single-phase boost PFC voltage-doubler self-controlled using FPGA. , 2008, , .		5
30	An FPGA control application: Self-control of current and linear control of DC link of PFC. , 2008, , .		4
31	Ac Indirect Line Conditioner Digital Control Using PLL Based on the Three-Phase Instantaneous Power Theory. Industrial Electronics Society (IECON), Annual Conference of IEEE, 2006, , .	0.0	1