

Sriram S R

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/110965/publications.pdf>

Version: 2024-02-01

9
papers

46
citations

1937685

4
h-index

1872680

6
g-index

9
all docs

9
docs citations

9
times ranked

25
citing authors

#	ARTICLE	IF	CITATIONS
1	An Analytical Model of Single-Event Transients in Double-Gate MOSFET for Circuit Simulation. IEEE Transactions on Electron Devices, 2019, 66, 3710-3717.	3.0	14
2	Impact of NBTI induced variations on delay locked loop multi-phase clock generator. Microelectronics Reliability, 2016, 60, 33-40.	1.7	8
3	A physics-based 3-D potential and threshold voltage model for undoped triple-gate FinFET with interface trapped charges. Journal of Computational Electronics, 2019, 18, 37-45.	2.5	8
4	Analytical model of hot carrier degradation in uniaxial strained triple-gate FinFET for circuit simulation. Journal of Computational Electronics, 2018, 17, 163-171.	2.5	7
5	A physics-based model for LER-induced threshold voltage variations in double-gate MOSFET. Journal of Computational Electronics, 2020, 19, 622-630.	2.5	5
6	Analytical Model for RDF-Induced Threshold Voltage Fluctuations in Double-Gate MOSFET. IEEE Transactions on Device and Materials Reliability, 2019, 19, 370-377.	2.0	3
7	Hot Carrier Reliability in 45 nm Strained Si/relaxed Si _{1-x} Ge _x CMOS Based SRAM Cell. , 2018, , .		1
8	Design of FinFET based frequency synthesizer. , 2015, , .		0
9	Impact of NBTI induced variations on FinFET based Vernier delay line time to digital converter. , 2017, , .		0