## Viranjay M Srivastava, Pr Eng

List of Publications by Year in descending order

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218 papers 1,170 citations

567281 15 h-index 642732 23 g-index

220 all docs

220 docs citations

times ranked

220

582 citing authors

#	Article	lF	Citations
1	Design and Performance Analysis of Low Sub-threshold Swing p-Channel Cylindrical Thin-film Transistors. Micro and Nanosystems, 2023, 15, 65-74.	0.6	O
2	Realization with fabrication of double-gate MOSFET based boost regulator. Materials Today: Proceedings, 2022, , .	1.8	1
3	Mathematical modeling of electron density arrangement in CSDG MOSFET: a nano-material approach. Journal of Materials Science, 2022, 57, 8381-8392.	3.7	7
4	Design and performance analysis of SiO2-MOSFET based absorber for reflected RF signal. Materials Today: Proceedings, 2022, , .	1.8	0
5	Realization with Fabrication of Dual-Gate MOSFET Based Source Follower. Silicon, 2022, 14, 11979-11989.	3.3	6
6	Effect of Metal Gate Work Function Variation on Underlap FinFET., 2022,,.		1
7	Regression Analysis of Static Noise Margin and Transconductance for Underlap Lengths of FinFET. , 2022, , .		O
8	Performance Analysis of Learning Rate Parameter on Prediction of Signal Power Loss for Network Optimization and Better Generalization. Wireless Personal Communications, 2021, 118, 1111-1128.	2.7	7
9	Realization with Fabrication of Double-Gate MOSFET Based Buck Regulator. International Journal of Electrical and Electronic Engineering and Telecommunications, 2021, , 66-75.	3.6	7
10	Compact Noise Modeling Approach in Nano-scaled CSDG MOSFET for RF Switch. Journal of Physics: Conference Series, 2021, 1714, 012024.	0.4	1
11	Effect of Architectural Composition of MLP ANN in Neural Network Learning for Signal Power Loss Prediction. Journal of Communications, 2021, , 20-29.	1.6	3
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13	Capacitive Modeling of Cylindrical Surrounding Double-Gate MOSFETs for Hybrid RF Applications. IEEE Access, 2021, 9, 89234-89242.	4.2	16
14	Assessment of Quantum Scaling Length Model for Cylindrical Surrounding Double-Gate (CSDG) MOSFET. Micro and Nanosystems, 2021, 13, 467-472.	0.6	4
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16	Third Order Band Pass Filter With Double-Gate MOSFET: A Simulation Perspective. IOP Conference Series: Materials Science and Engineering, 2021, 1126, 012019.	0.6	0
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18	Prediction of Signal Power Loss (at Micro-Cell Environments) Using Generalized Adaptive Regression and Radial Basis Function ANN Models Built on Filter for Error Correction. International Journal on Communications Antenna and Propagation, 2021, 11, 94.	0.3	1

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19	An Efficient Model for Low Voltage Distribution Smart Feeder Overcurrent Protection System Using Microcontroller. IOP Conference Series: Materials Science and Engineering, 2021, 1152, 012015.	0.6	O
20	Improvement of CuO Thin Film Properties for High Mobility p-Channel TFT Applications. , 2021, , .		3
21	Study of SiGe-Si Source Stacked in Silicon Nano-Tube Tunnel FET. , 2021, , .		3
22	Analysis of Nanometer-Scale n-Type Double-Gate (DG) MOSFETs Using High-Æ™ Dielectrics for High-Speed Applications. , 2021, , .		9
23	Thickness Modeling of Short-Channel Cylindrical Surrounding Double- Gate MOSFET at Strong Inversion Using Depletion Depth Analysis. Micro and Nanosystems, 2021, 13, 319-325.	0.6	3
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27	InP/AlGaAs Based CSDG MOSFET With Au/Pt Gate Materials for High Frequency/Hybrid Applications. , 2021, , .		1
28	Designing of Novel Eighth-Mode Forth-Order Substrate Integrated Waveguide Band-Pass Filter with High Selectivity. Journal of Communications, 2021, , 553-558.	1.6	2
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33	Effect of Drain Splitting on Silicon Nano-Tube Tunnel Field Effect Transistor. , 2021, , .		O
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36	Bandwidth and Return Loss Improvement Technique Using Double-Material Substrate Cylindrical Surrounding Patch Antenna:Part-I. SSRG International Journal of Engineering Trends and Technology, 2021, 69, 252-256.	0.5	0

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38	Prediction and avoidance of real-time traffic congestion system for Indian metropolitan cities. International Journal of Vehicle Information and Communication Systems, 2020, 5, 109.	0.1	O
39	Designing of Buck Regulator with Double-Gate MOSFET: A Circuit Perspective. , 2020, , .		1
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42	Design of Double-Gate Tri-Active Layer Channel Based IGZO Thin-Film Transistor for Improved Performance of Ultra-Low-Power RFID Rectifier. IEEE Access, 2020, 8, 194652-194662.	4.2	12
43	Third Order High Pass Filter with Double-Gate MOSFET: A Circuit Perspective. , 2020, , .		O
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45	Realization with Fabrication of Double-Gate MOSFET Based Third Order High Pass Filter. International Journal of Electrical and Electronic Engineering and Telecommunications, 2020, , 213-222.	3.6	11
46	An Improved Handover Algorithm for LTE-A Femtocell Network. Journal of Communications, 2020, , 558-565.	1.6	2
47	Modeling Environmental Effects on Electromagnetic Signal Propagation Using Multi-Layer Perceptron Artificial Neural Network. International Journal on Communications Antenna and Propagation, 2020, 10, 175.	0.3	0
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49	An Adaptive Vector Median Filtering Approach to Enhance the Prediction Efficiency of Signal Power Loss. Journal of Communications, 2020, , 866-875.	1.6	1
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88	Effect of Gate-lap and Oxide Material at 10-nm FinFET Device Performance. , 2018, , .		0
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94	Dielectric Material (HfO2) Effect on Surface Potential for CSDG MOSFET., 2018,,.	_	7
95	Concept of Double-Pole Four-Throw Switch with n-MOSFETs. , 2018, , .		0
96	Effect of radius on various parameters of cylindrical surrounding double-gate (CSDG) MOSFET. International Journal of Engineering and Technology(UAE), 2018, 7, 2127.	0.3	2
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115	Optical spatial modulation over Gamma–Gamma turbulence and pointing error induced fading channels. Optik, 2017, 147, 214-223.	2.9	19
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132	A comparison between mathematical tools for analyzing FSO systems over Gamma-Gamma atmospheric channel. , 2017, , .		9
133	Automatic test data generation based on multi-objective ant lion optimization algorithm., 2017,,.		5
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