

Steven Swanson

List of Publications by Year in descending order

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39
papers

1,940
citations

1039406

9
h-index

887659

17
g-index

39
all docs

39
docs citations

39
times ranked

1061
citing authors

#	ARTICLE	IF	CITATIONS
1	Characterizing flash memory. , 2009, , .		339
2	Conservation cores. , 2010, , .		265
3	Moneta: A High-Performance Storage Array Architecture for Next-Generation, Non-volatile Memories. , 2010, , .		207
4	Gordon. , 2009, , .		156
5	The GreenDroid Mobile Application Processor: An Architecture for Silicon's Dark Future. IEEE Micro, 2011, 31, 86-95.	1.8	119
6	Understanding the Impact of Emerging Non-Volatile Memories on High-Performance, IO-Intensive Computing. , 2010, , .		104
7	Providing safe, user space access to fast, solid state disks. , 2012, , .		97
8	Refactor, Reduce, Recycle: Restructuring the I/O Stack for the Future of Storage. Computer, 2013, 46, 52-59.	1.2	55
9	An Experimental Study of Bitmap Compression vs. Inverted List Compression. , 2017, , .		49
10	Gordon. ACM SIGPLAN Notices, 2009, 44, 217-228.	0.2	46
11	KAML: A Flexible, High-Performance Key-Value SSD. , 2017, , .		45
12	Rethinking Flash in the Data Center. IEEE Micro, 2010, 30, 52-54.	1.8	43
13	Minerva: Accelerating Data Analysis in Next-Generation SSDs. , 2013, , .		42
14	HippogriffDB. Proceedings of the VLDB Endowment, 2016, 9, 1647-1658.	2.1	40
15	SSD in-storage computing for list intersection. , 2016, , .		39
16	Modeling Power Consumption of NAND Flash Memories Using FlashPower. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2013, 32, 1031-1044.	1.9	35
17	QuickSAN. , 2013, , .		35
18	Morpheus. Computer Architecture News, 2016, 44, 53-65.	2.5	33

#	ARTICLE	IF	CITATIONS
19	Efficient complex operators for irregular codes. , 2011, , .		24
20	SSD In-Storage Computing for Search Engines. IEEE Transactions on Computers, 2024, , 1-1.	2.4	22
21	MILC. Proceedings of the VLDB Endowment, 2017, 10, 853-864.	2.1	19
22	GreenDroid: An architecture for the Dark Silicon Age. , 2012, , .		17
23	Morpheus: Creating Application Objects Efficiently for Heterogeneous Computing. , 2016, , .		17
24	Latency-Optimized Networks for Clustering FPGAs. , 2013, , .		13
25	Welcome to the Entropics: Boot-Time Entropy in Embedded Devices. , 2013, , .		10
26	QuickSAN. Computer Architecture News, 2013, 41, 464-474.	2.5	9
27	Griffin. , 2018, , .		8
28	Breeze: User-Level Access to Non-Volatile Main Memories for Legacy Software. , 2018, , .		8
29	Gordon: An Improved Architecture for Data-Intensive Applications. IEEE Micro, 2010, 30, 121-130.	1.8	7
30	Hippogriff: Efficiently moving data in heterogeneous computing systems. , 2016, , .		7
31	Providing safe, user space access to fast, solid state disks. ACM SIGPLAN Notices, 2012, 47, 387-400.	0.2	7
32	Exploring Energy Scalability in Coprocessor-Dominated Architectures for Dark Silicon. Transactions on Embedded Computing Systems, 2014, 13, 1-24.	2.1	5
33	Bankshot. Operating Systems Review (ACM), 2014, 48, 73-81.	1.5	4
34	An Evaluation of Selective Depipelining for FPGA-Based Energy-Reducing Irregular Code Coprocessors. , 2011, , .		3
35	SPMario: Scale up MapReduce with I/O-Oriented Scheduling for the GPU. , 2016, , .		3
36	Griffin. ACM SIGPLAN Notices, 2018, 53, 327-337.	0.2	3

#	ARTICLE	IF	CITATIONS
37	Reducing the Energy Cost of Irregular Code Bases in Soft Processor Systems. , 2011, , .		2
38	Underclocked Software Prefetching: More Cores, Less Energy. IEEE Micro, 2012, 32, 32-41.	1.8	2
39	Morpheus. Operating Systems Review (ACM), 2018, 52, 71-83.	1.5	1