

Jason Oberg

List of Publications by Year in descending order

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11
papers

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1306789

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11
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11
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11
times ranked

195
citing authors

#	ARTICLE	IF	CITATIONS
1	Accelerating Viola-Jones Face Detection to FPGA-Level Using GPUs. , 2010, , .		74
2	Detecting Hardware Trojans with Gate-Level Information-Flow Tracking. Computer, 2016, 49, 44-52.	1.2	70
3	Theoretical Fundamentals of Gate Level Information Flow Tracking. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2011, 30, 1128-1140.	1.9	49
4	Leveraging Gate-Level Properties to Identify Hardware Timing Channels. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2014, 33, 1288-1301.	1.9	39
5	Gate-Level Information Flow Tracking for Security Lattices. ACM Transactions on Design Automation of Electronic Systems, 2014, 20, 1-25.	1.9	29
6	On the Complexity of Generating Gate Level Information Flow Tracking Logic. IEEE Transactions on Information Forensics and Security, 2012, 7, 1067-1080.	4.5	28
7	Random decision tree body part recognition using FPGAs. , 2012, , .		25
8	Expanding Gate Level Information Flow Tracking for Multilevel Security. IEEE Embedded Systems Letters, 2013, 5, 25-28.	1.3	14
9	Eliminating Timing Information Flows in a Mix-Trusted System-on-Chip. IEEE Design and Test, 2013, 30, 55-62.	1.1	12
10	A Practical Testing Framework for Isolating Hardware Timing Channels. , 2013, , .		11
11	Seeds of SEED: Building and Verifying Foundationally Isolated Hardware Architectures. , 2021, , .		0