

# Etienne Nowak

## List of Publications by Year in descending order

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18  
papers

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citations

933447

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1125743

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18  
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18  
docs citations

18  
times ranked

546  
citing authors

#	ARTICLE	IF	CITATIONS
1	Experimental Investigation of 4-kb RRAM Arrays Programming Conditions Suitable for TCAM. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2599-2607.	3.1	59
2	Digital Biologically Plausible Implementation of Binarized Neural Networks With Differential Hafnium Oxide Resistive Memory Arrays. Frontiers in Neuroscience, 2019, 13, 1383.	2.8	51
3	Resistive RAM Endurance: Array-Level Characterization and Correction Techniques Targeting Deep Learning Applications. IEEE Transactions on Electron Devices, 2019, 66, 1281-1288.	3.0	43
4	Hybrid RRAM toward Next Generation of Nonvolatile Memory: Coupling of Oxygen Vacancies and Metal Ions. Advanced Electronic Materials, 2019, 5, 1800658.	5.1	37
5	Advances in Emerging Memory Technologies: From Data Storage to Artificial Intelligence. Applied Sciences (Switzerland), 2021, 11, 11254.	2.5	28
6	(Invited) Resistive Memories (RRAM) Variability: Challenges and Solutions. ECS Transactions, 2018, 86, 35-47.	0.5	22
7	Sub-pJ consumption and short latency time in RRAM arrays for high endurance applications. , 2018, , .		22
8	Role of synaptic variability in resistive memory-based spiking neural networks with unsupervised learning. Journal Physics D: Applied Physics, 2018, 51, 444002.	2.8	21
9	Endurance Statistical Behavior of Resistive Memories Based on Experimental and Theoretical Investigation. IEEE Transactions on Electron Devices, 2019, 66, 3318-3325.	3.0	19
10	Liquid Silicon: A Nonvolatile Fully Programmable Processing-in-Memory Processor With Monolithically Integrated ReRAM. IEEE Journal of Solid-State Circuits, 2020, 55, 908-919.	5.4	17
11	Illusion of large on-chip memory by networked computing chips for neural network inference. Nature Electronics, 2021, 4, 71-80.	26.0	15
12	14.3 A 43pJ/Cycle Non-Volatile Microcontroller with 4.7 $\mu$ s Shutdown/Wake-up Integrating 2.3-bit/Cell Resistive RAM and Resilience Techniques. , 2019, , .		13
13	Novel Computing Method for Short Programming Time and Low Energy Consumption in HfO <sub>2</sub> Based RRAM Arrays. IEEE Journal of the Electron Devices Society, 2018, 6, 696-702.	2.1	10
14	Phase-Change Memory: Performance, Roles and Challenges. , 2018, , .		8
15	Optimizing Programming Energy for Improved RRAM Reliability for High Endurance Applications. , 2018, , .		6
16	Nano-analytical investigation of the forming process in an HfO <sub>2</sub> -based resistive switching memory. Journal of Applied Physics, 2021, 130, .	2.5	5
17	Model of the Weak Reset Process in HfO <sub>x</sub> Resistive Memory for Deep Learning Frameworks. IEEE Transactions on Electron Devices, 2021, 68, 4925-4932.	3.0	3
18	Memory-Centric Neuromorphic Computing With Nanodevices. , 2019, , .		0