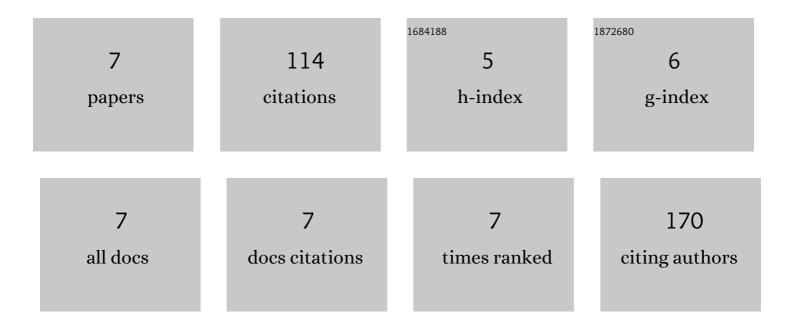
Emilio José Lupon

List of Publications by Year in descending order

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ΕΜΙΙΙΟ ΙΟSÃO LUDON

#	Article	IF	CITATIONS
1	Enhanced DC-Link Capacitor Voltage Balancing Control of DC–AC Multilevel Multileg Converters. IEEE Transactions on Industrial Electronics, 2015, 62, 2663-2672.	7.9	48
2	FPGA Implementation of a PWM for a Three-Phase DC–AC Multilevel Active-Clamped Converter. IEEE Transactions on Industrial Informatics, 2014, 10, 1296-1306.	11.3	28
3	Verifying Functional Specifications by Regression Techniques on Lissajous Test Signatures. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 754-762.	5.4	18
4	A Calibratable Detector for Invasive Attacks. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 1067-1079.	3.1	14
5	FPGA-Based Controller for a Permanent-Magnet Synchronous Motor Drive Based on a Four-Level Active-Clamped DC-AC Converter. Energies, 2018, 11, 2639.	3.1	5
6	Indirect and adaptive test of analogue circuits based on preselected steadyâ€ s tate response measures. IET Circuits, Devices and Systems, 2020, 14, 611-618.	1.4	1
7	Crypto-Test-Lab for security validation of ECC co-processor test infrastructure. , 2017, , .		0