

# Mei Yang

## List of Publications by Year in Descending Order

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**Version:** 2024-04-27

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

42 papers	846 citations	15 h-index	28 g-index
43 ext. papers	1,035 ext. citations	3.2 avg, IF	3.67 L-index

#	Paper	IF	Citations
42	On hardware-trojan-assisted power budgeting system attack targeting many core systems. <i>Journal of Systems Architecture</i> , <b>2020</b> , 109, 101757	5.5	4
41	Combating Enhanced Thermal Covert Channel in Multi-/Many-Core Systems With Channel-Aware Jamming. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , <b>2020</b> , 39, 3276-3287	3.5	4
40	On Countermeasures Against the Thermal Covert Channel Attacks Targeting Many-core Systems <b>2020</b> ,		3
39	Dynamic Allocation/Reallocation of Dark Cores in Many-Core Systems for Improved System Performance. <i>IEEE Access</i> , <b>2020</b> , 8, 165693-165707	3.5	2
38	Mercury flows in large-scale gold production and implications for Hg pollution control. <i>Journal of Environmental Sciences</i> , <b>2018</b> , 68, 91-99	6.4	21
37	Effectiveness of HT-assisted sinkhole and blackhole denial of service attacks targeting mesh networks-on-chip. <i>Journal of Systems Architecture</i> , <b>2018</b> , 89, 84-94	5.5	15
36	Mercury emission and speciation from industrial gold production using roasting process. <i>Journal of Geochemical Exploration</i> , <b>2016</b> , 170, 72-77	3.8	7
35	Mercury transformation and speciation in flue gases from anthropogenic emission sources: a critical review. <i>Atmospheric Chemistry and Physics</i> , <b>2016</b> , 16, 2417-2433	6.8	84
34	. <i>IEEE Transactions on Computers</i> , <b>2016</b> , 65, 2780-2793	2.5	8
33	A pareto-optimal runtime power budgeting scheme for many-core systems. <i>Microprocessors and Microsystems</i> , <b>2016</b> , 46, 136-148	2.4	3
32	An efficient runtime power allocation scheme for many-core systems inspired from auction theory. <i>The Integration VLSI Journal</i> , <b>2015</b> , 50, 147-157	1.4	2
31	Updated emission inventories for speciated atmospheric mercury from anthropogenic sources in China. <i>Environmental Science &amp; Technology</i> , <b>2015</b> , 49, 3185-94	10.3	285
30	A Hybrid Optoelectronic Networks-on-Chip Architecture. <i>Journal of Lightwave Technology</i> , <b>2014</b> , 32, 991-998	4	25
29	On self-tuning networks-on-chip for dynamic network-flow dominance adaptation. <i>Transactions on Embedded Computing Systems</i> , <b>2014</b> , 13, 1-21	1.8	14
28	On Reducing Insertion Loss in Wavelength-Routed Optical Network-on-Chip Architecture. <i>Journal of Optical Communications and Networking</i> , <b>2014</b> , 6, 879	4.1	6
27	On self-tuning networks-on-chip for dynamic network-flow dominance adaptation <b>2013</b> ,		16
26	Efficient multicast schemes for 3-D Networks-on-Chip. <i>Journal of Systems Architecture</i> , <b>2013</b> , 59, 693-708	5.5	2

25	Four-Port Silicon Multi-Wavelength Optical Router for Photonic Networks-on-Chip. <i>IEEE Photonics Technology Letters</i> , <b>2013</b> , 25, 2281-2284	2.2	25
24	Tunable Fano resonances based on two-beam interference in microring resonator. <i>Applied Physics Letters</i> , <b>2013</b> , 102, 011112	3.4	45
23	Energy Efficient Run-Time Incremental Mapping for 3-D Networks-on-Chip. <i>Journal of Computer Science and Technology</i> , <b>2013</b> , 28, 54-71	1.7	18
22	Silicon photonic network-on-chip and enabling components. <i>Science China Technological Sciences</i> , <b>2013</b> , 56, 543-553	3.5	6
21	Wavelength-routed optical networks-on-chip built with comb switches <b>2013</b> ,		2
20	Scalable-Grain Pipeline Parallelization Method for Multi-core Systems. <i>Lecture Notes in Computer Science</i> , <b>2013</b> , 269-283	0.9	
19	A Generic Optical Router Design for Photonic Network-on-Chips. <i>Journal of Lightwave Technology</i> , <b>2012</b> , 30, 368-376	4	60
18	Circuit-switched on-chip photonic interconnection network <b>2012</b> ,		5
17	On a joint temporal-spatial multi-channel assignment and routing scheme in resource-constrained wireless mesh networks. <i>Ad Hoc Networks</i> , <b>2012</b> , 10, 401-420	4.8	4
16	On-chip wavelength-routed photonic networks with comb switches <b>2012</b> ,		4
15	On a Scalable, Non-Blocking Optical Router for Photonic Networks-on-Chip Designs <b>2011</b> ,		20
14	Wavelength-selective 4 × 4 nonblocking silicon optical router for networks-on-chip. <i>Optics Letters</i> , <b>2011</b> , 36, 4710-2	3	44
13	On an efficient NoC multicasting scheme in support of multiple applications running on irregular sub-networks. <i>Microprocessors and Microsystems</i> , <b>2011</b> , 35, 119-129	2.4	16
12	Low latency and energy efficient multicasting schemes for 3D NoC-based SoCs <b>2011</b> ,		9
11	Power-Aware Run-Time Incremental Mapping for 3-D Networks-on-Chip. <i>Lecture Notes in Computer Science</i> , <b>2011</b> , 232-247	0.9	3
10	A power-aware mapping approach to map IP cores onto NoCs under bandwidth and latency constraints. <i>Transactions on Architecture and Code Optimization</i> , <b>2010</b> , 7, 1-30	1.3	21
9	Building a multi-FPGA-based emulation framework to support networks-on-chip design and verification. <i>International Journal of Electronics</i> , <b>2010</b> , 97, 1241-1262	1.2	12
8	Efficient multicasting scheme for irregular mesh-based NoCs <b>2010</b> ,		3

- 7 . *IEEE/ACM Transactions on Networking*, **2010**, 18, 1436-1449 3.8 6
- 6 Network interface design based on mutual interface definition. *International Journal of High Performance Systems Architecture*, **2010**, 2, 168 0.9 2
- 5 HTSMA: A Hybrid Temporal-Spatial Multi-Channel Assignment Scheme in Heterogeneous Wireless Mesh Networks **2009**, 1
- 4 Architectures and routing schemes for optical network-on-chips. *Computers and Electrical Engineering*, **2009**, 35, 856-877 4.3 18
- 3 Dual-Homing Based Scalable Partial Multicast Protection. *IEEE Transactions on Computers*, **2006**, 55, 1130-1141 15 17
- 2 A RDT-based interconnection network for scalable network-on-chip designs **2005**, 2
- 1 On finding the best partial multicast protection tree under dual-homing architecture 2