

# C Vasanthanayaki

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10971363/publications.pdf>

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8  
papers

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citations

1937685

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1720034

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g-index

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docs citations

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times ranked

30  
citing authors

#	ARTICLE	IF	CITATIONS
1	High Performance Error Tolerant Adders for Image Processing Applications. IETE Journal of Research, 2021, 67, 205-216.	2.6	18
2	High performance compact energy efficient error tolerant adders and multipliers for 16-bit image processing applications. Microprocessors and Microsystems, 2020, 78, 103237.	2.8	22
3	High Performance Four Segment Error Tolerant Adder for 8-bit Pixel Depth Image Processing Applications. Journal of Signal Processing Systems, 2020, 92, 693-703.	2.1	3
4	High Speed Energy Efficient Static Segment Adder for Approximate Computing Applications. Journal of Electronic Testing: Theory and Applications (JETTA), 2017, 33, 125-132.	1.2	8
5	High Performance Significance Approximation Error Tolerance Adder for Image Processing Applications. Journal of Electronic Testing: Theory and Applications (JETTA), 2016, 32, 377-383.	1.2	21
6	Image coding using lapped biorthogonal transform. Signal, Image and Video Processing, 2013, 7, 879-888.	2.7	2
7	FPGA based efficient on-chip memory for image processing algorithms. Microelectronics Journal, 2012, 43, 916-928.	2.0	3
8	Comparison and extension of high performance adders for hybrid and error tolerant applications. Journal of Ambient Intelligence and Humanized Computing, 0, , 1.	4.9	0