

C Vasanthanayaki

List of Publications by Year in descending order

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Version: 2024-02-01

8
papers

77
citations

1937685

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1720034

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docs citations

8
times ranked

30
citing authors

#	ARTICLE	IF	CITATIONS
1	High performance compact energy efficient error tolerant adders and multipliers for 16-bit image processing applications. <i>Microprocessors and Microsystems</i> , 2020, 78, 103237.	2.8	22
2	High Performance Significance Approximation Error Tolerance Adder for Image Processing Applications. <i>Journal of Electronic Testing: Theory and Applications (JETTA)</i> , 2016, 32, 377-383.	1.2	21
3	High Performance Error Tolerant Adders for Image Processing Applications. <i>IETE Journal of Research</i> , 2021, 67, 205-216.	2.6	18
4	High Speed Energy Efficient Static Segment Adder for Approximate Computing Applications. <i>Journal of Electronic Testing: Theory and Applications (JETTA)</i> , 2017, 33, 125-132.	1.2	8
5	FPGA based efficient on-chip memory for image processing algorithms. <i>Microelectronics Journal</i> , 2012, 43, 916-928.	2.0	3
6	High Performance Four Segment Error Tolerant Adder for 8-bit Pixel Depth Image Processing Applications. <i>Journal of Signal Processing Systems</i> , 2020, 92, 693-703.	2.1	3
7	Image coding using lapped biorthogonal transform. <i>Signal, Image and Video Processing</i> , 2013, 7, 879-888.	2.7	2
8	Comparison and extension of high performance adders for hybrid and error tolerant applications. <i>Journal of Ambient Intelligence and Humanized Computing</i> , 0, , 1.	4.9	0