

# Jianhua Li

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10969332/publications.pdf>

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4  
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2682572

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31  
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| # | ARTICLE   | IF  | CITATIONS |
|---|---|-----|-----------|
| 1 | Thread Criticality Assisted Replication and Migration for Chip Multiprocessor Caches. IEEE Transactions on Computers, 2017, 66, 1747-1762.              | 3.4 | 3         |
| 2 | Dual partitioning multicasting for high-performance on-chip networks. Journal of Parallel and Distributed Computing, 2014, 74, 1858-1871.               | 4.1 | 2         |
| 3 | A Unified Write Buffer Cache Management Scheme for Flash Memory. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 2779-2792. | 3.1 | 10        |
| 4 | LADPM: Latency-Aware Dual-Partition Multicast Routing for Mesh-Based Network-on-Chips. , 2010, , .  |     | 8         |