

# Alok Kumar Mishra

## List of Publications by Year in descending order

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2258059

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#	ARTICLE	IF	CITATIONS
1	A Partially Static High Frequency 18T Hybrid Topological Flip-Flop Design for Low Power Application. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 1592-1596.	3.0	15
2	Design and analysis of ultra-low power 18T adaptive data track flip-flop for high-speed application. International Journal of Circuit Theory and Applications, 2021, 49, 3733-3747.	2.0	9
3	Performance analysis of multi-scaling voltage level shifter for low-power applications. World Journal of Engineering, 2020, 17, 803-809.	1.6	0
4	Performance Analysis of Implicit Pulsed and Low-Glitch Power-Efficient Double-Edge-Triggered Flip-Flops Using C-Elements. Lecture Notes in Electrical Engineering, 2020, , 261-275.	0.4	1
5	A novel approach for noise tolerant energy efficient TSPC dynamic circuit design. Analog Integrated Circuits and Signal Processing, 2019, 100, 119-131.	1.4	3