

Deming Zhang

List of Publications by Year in descending order

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168
papers

4,101
citations

147566

31
h-index

143772

57
g-index

170
all docs

170
docs citations

170
times ranked

2897
citing authors

#	ARTICLE	IF	CITATIONS
1	Compact Modeling of Perpendicular-Anisotropy CoFeB/MgO Magnetic Tunnel Junctions. IEEE Transactions on Electron Devices, 2012, 59, 819-826.	1.6	330
2	Current-induced magnetization switching in atom-thick tungsten engineered perpendicular magnetic tunnel junctions with large tunnel magnetoresistance. Nature Communications, 2018, 9, 671.	5.8	259
3	Long-distance propagation of short-wavelength spin waves. Nature Communications, 2018, 9, 738.	5.8	181
4	Reconfigurable Codesign of STT-MRAM Under Process Variations in Deeply Scaled Technology. IEEE Transactions on Electron Devices, 2015, 62, 1769-1777.	1.6	135
5	Compact Model of Dielectric Breakdown in Spin-Transfer Torque Magnetic Tunnel Junction. IEEE Transactions on Electron Devices, 2016, 63, 1762-1767.	1.6	132
6	High-Density NAND-Like Spin Transfer Torque Memory With Spin Orbit Torque Erase Operation. IEEE Electron Device Letters, 2018, 39, 343-346.	2.2	119
7	Spintronics for Energy- Efficient Computing: An Overview and Outlook. Proceedings of the IEEE, 2021, 109, 1398-1417.	16.4	112
8	Field-free spin-orbit torque-induced switching of perpendicular magnetization in a ferrimagnetic layer with a vertical composition gradient. Nature Communications, 2021, 12, 4555.	5.8	105
9	Exchange bias switching in an antiferromagnet/ferromagnet bilayer driven by spin-orbit torque. Nature Electronics, 2020, 3, 757-764.	13.1	99
10	A compact skyrmionic leaky-integrate-fire spiking neuron device. Nanoscale, 2018, 10, 6139-6146.	2.8	96
11	Synchronous Non-Volatile Logic Gate Design Based on Resistive Switching Memories. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 443-454.	3.5	90
12	Robust Ultra-Low Power Non-Volatile Logic-in-Memory Circuits in FD-SOI Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 847-857.	3.5	85
13	All Spin Artificial Neural Networks Based on Compound Spintronic Synapse and Neuron. IEEE Transactions on Biomedical Circuits and Systems, 2016, 10, 828-836.	2.7	84
14	Compact Model of Subvolume MTJ and Its Design Application at Nanoscale Technology Nodes. IEEE Transactions on Electron Devices, 2015, 62, 2048-2055.	1.6	78
15	Stateful Reconfigurable Logic via a Single-Voltage-Gated Spin Hall-Effect Driven Magnetic Tunnel Junction in a Spintronic Memory. IEEE Transactions on Electron Devices, 2017, 64, 4295-4301.	1.6	76
16	Failure Analysis in Magnetic Tunnel Junction Nanopillar with Interfacial Perpendicular Magnetic Anisotropy. Materials, 2016, 9, 41.	1.3	72
17	Skyrmions in Magnetic Tunnel Junctions. ACS Applied Materials & Interfaces, 2018, 10, 16887-16892.	4.0	68
18	Recent progress of integrated circuits and optoelectronic chips. Science China Information Sciences, 2021, 64, 1.	2.7	56

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19	Synchronous 8-bit Non-Volatile Full-Adder based on Spin Transfer Torque Magnetic Tunnel Junction. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 1757-1765.	3.5	50
20	eSLAM. , 2019, , .		47
21	Compact Modeling and Analysis of Voltage-Gated Spin-Orbit Torque Magnetic Tunnel Junction. IEEE Access, 2020, 8, 50792-50800.	2.6	46
22	Dynamics of a magnetic skyrmionium driven by spin waves. Applied Physics Letters, 2018, 112, .	1.5	43
23	Tunnel Junction with Perpendicular Magnetic Anisotropy: Status and Challenges. Micromachines, 2015, 6, 1023-1045.	1.4	41
24	Skyrmion Racetrack Memory With Random Information Update/Deletion/Insertion. IEEE Transactions on Electron Devices, 2018, 65, 87-95.	1.6	41
25	Temperature Impact Analysis and Access Reliability Enhancement for 1T1MTJ STT-RAM. IEEE Transactions on Reliability, 2016, 65, 1755-1768.	3.5	40
26	Gate-Driven Pure Spin Current in Graphene. Physical Review Applied, 2017, 8, .	1.5	39
27	Generation and Control of Terahertz Spin Currents in Topology-Induced 2D Ferromagnetic Fe ₃ GeTe ₂ Bi ₂ Te ₃ Heterostructures. Advanced Materials, 2022, 34, e2106172.	11.1	39
28	High-Speed, Low-Power, Magnetic Non-Volatile Flip-Flop With Voltage-Controlled, Magnetic Anisotropy Assistance. IEEE Magnetics Letters, 2016, 7, 1-5.	0.6	38
29	In-Memory Processing Paradigm for Bitwise Logic Operations in STT-MRAM. IEEE Transactions on Magnetics, 2017, 53, 1-4.	1.2	36
30	A Novel MTJ-Based Non-Volatile Ternary Content-Addressable Memory for High-Speed, Low-Power, and High-Reliable Search Operation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1454-1464.	3.5	35
31	Addressing the Thermal Issues of STT-MRAM From Compact Modeling to Design Techniques. IEEE Nanotechnology Magazine, 2018, 17, 345-352.	1.1	33
32	Sub-ns Field-Free Switching in Perpendicular Magnetic Tunnel Junctions by the Interplay of Spin Transfer and Orbit Torques. IEEE Electron Device Letters, 2021, 42, 704-707.	2.2	33
33	Tunable Tunneling Magnetoresistance in van der Waals Magnetic Tunnel Junctions with CrTe ₂ Electrodes. ACS Applied Materials & Interfaces, 2021, 13, 1214-1221.	4.0	33
34	A true random number generator based on parallel STT-MTJs. , 2017, , .		31
35	Design and Fabrication of Full Wheatstone-Bridge-Based Angular GMR Sensors. Sensors, 2018, 18, 1832.	2.1	30
36	Skyrmion dynamics in width-varying nanotracks and implications for skyrmionic applications. Applied Physics Letters, 2017, 111, .	1.5	29

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37	A Self-Timed Voltage-Mode Sensing Scheme With Successive Sensing and Checking for STT-MRAM. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 1602-1614.	3.5	29
38	Developments and applications of tunneling magnetoresistance sensors. Tsinghua Science and Technology, 2022, 27, 443-454.	4.1	28
39	Direct Observation of Domain-Wall Surface Tension by Deflating or Inflating a Magnetic Bubble. Physical Review Applied, 2018, 9, .	1.5	27
40	Quantitative modeling of racetrack memory, a tradeoff among area, performance, and power. , 2015, , .		26
41	Compact Modeling and Evaluation of Magnetic Skyrmion-Based Racetrack Memory. IEEE Transactions on Electron Devices, 2017, 64, 1060-1068.	1.6	26
42	Interfacial Perpendicular Magnetic Anisotropy in Sub-20 nm Tunnel Junctions for Large-Capacity Spin-Transfer Torque Magnetic Random-Access Memory. IEEE Magnetism Letters, 2017, 8, 1-5.	0.6	25
43	Picosecond optospintronic tunnel junctions. Proceedings of the National Academy of Sciences of the United States of America, 2022, 119, .	3.3	25
44	A microwave field-driven transistor-like skyrmionic device with the microwave current-assisted skyrmion creation. Journal of Applied Physics, 2017, 122, .	1.1	24
45	Anomalous Nernst effect in Ir ₂₂ Mn ₇₈ /Co ₂₀ Fe ₆₀ B ₂₀ /MgO layers with perpendicular magnetic anisotropy. Applied Physics Letters, 2017, 111, .	1.5	24
46	Variation-Resilient True Random Number Generators Based on Multiple STT-MTJs. IEEE Nanotechnology Magazine, 2018, 17, 1270-1281.	1.1	24
47	Anomalous Hall and Nernst Effects in Co_2TiSn and Co_2Ti . Physical Review Applied, 2018, 10, .	1.5	24
48	Epitaxial Growth of Aligned and Continuous Carbon Nanofibers from Carbon Nanotubes. ACS Nano, 2017, 11, 1257-1263.	7.3	23
49	Air-Optical Helicity-Independent Switching State Diagram in GdFeCo . Physical Review Applied, 2018, 10, .	1.5	23
50	Separation of emission mechanisms in spintronic terahertz emitters. Physical Review B, 2021, 104, .	1.1	22
51	Phase-change-assisted spin-transfer torque switching in perpendicular magnetic tunnel junctions. Applied Physics Letters, 2021, 119, .	1.5	22
52	Exploiting Spin-Orbit Torque Devices As Reconfigurable Logic for Circuit Obfuscation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 57-69.	1.9	21
53	Design of Magnetic Non-Volatile TCAM With Priority-Decision in Memory Technology for High Speed, Low Power, and High Reliability. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 464-474.	3.5	20
54	Exploiting Carbon Nanotube FET and Magnetic Tunneling Junction for Near-Memory-Computing Paradigm. IEEE Transactions on Electron Devices, 2021, 68, 1975-1979.	1.6	20

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55	Reliability-Enhanced Separated Pre-Charge Sensing Amplifier for Hybrid CMOS/MTJ Logic Circuits. IEEE Transactions on Magnetics, 2017, 53, 1-5.	1.2	19
56	Controlled Switching of the Number of Skyrmions in a Magnetic Nanodot by Electric Fields. Advanced Materials, 2022, 34, e2107908.	11.1	19
57	Perspectives of Racetrack Memory for Large-Capacity On-Chip Memory: From Device to System. IEEE Transactions on Circuits and Systems I: Regular Papers, 2016, 63, 629-638.	3.5	18
58	Reliability-Enhanced Hybrid CMOS/MTJ Logic Circuit Architecture. IEEE Transactions on Magnetics, 2017, 53, 1-5.	1.2	18
59	Exploring Hybrid STT-MTJ/CMOS Energy Solution in Near-/Sub-Threshold Regime for IoT Applications. IEEE Transactions on Magnetics, 2018, 54, 1-9.	1.2	18
60	Experimental Demonstration of NAND-Like Spin-Torque Memory Unit. IEEE Electron Device Letters, 2021, 42, 513-516.	2.2	18
61	Field-Free 3T2SOT MRAM for Non-Volatile Cache Memories. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4660-4669.	3.5	17
62	Forecasting the outcome of spintronic experiments with Neural Ordinary Differential Equations. Nature Communications, 2022, 13, 1016.	5.8	17
63	Large voltage-controlled magnetic anisotropy in the SrTiO ₃ /Fe/Cu structure. Applied Physics Letters, 2017, 111, 152403.	1.5	16
64	Optically Tunable Magnetoresistance Effect: From Mechanism to Novel Device Application. Materials, 2018, 11, 47.	1.3	16
65	Self-Adaptive Write Circuit for Magnetic Tunneling Junction Memory With Voltage-Controlled Magnetic Anisotropy Effect. IEEE Nanotechnology Magazine, 2018, 17, 492-499.	1.1	15
66	Hardware Acceleration Implementation of Sparse Coding Algorithm With Spintronic Devices. IEEE Nanotechnology Magazine, 2019, 18, 518-531.	1.1	15
67	Magnetic Nonvolatile SRAM Based on Voltage-Gated Spin-Orbit-Torque Magnetic Tunnel Junctions. IEEE Transactions on Electron Devices, 2020, 67, 1965-1971.	1.6	15
68	A Novel Computing-in-Memory Platform Based on Hybrid Spintronic/CMOS Memory. IEEE Transactions on Electron Devices, 2022, 69, 1698-1705.	1.6	15
69	High-Speed, Low-Power, and Error-Free Asynchronous Write Circuit for STT-MRAM and Logic. IEEE Transactions on Magnetics, 2016, 52, 1-4.	1.2	14
70	Scaling Study of Spin-Hall-Assisted Spin Transfer Torque Driven Magnetization Switching in the Presence of Dzyaloshinskii-Moriya Interaction. IEEE Nanotechnology Magazine, 2017, 16, 1138-1142.	1.1	14
71	Demonstration of Multi-State Memory Device Combining Resistive and Magnetic Switching Behaviors. IEEE Electron Device Letters, 2018, 39, 684-687.	2.2	14
72	Experimental demonstration of voltage-gated spin-orbit torque switching in an antiferromagnet/ferromagnet structure. Physical Review B, 2021, 103, .	1.1	14

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73	Prediction of crossing nodal-lines and large intrinsic spin Hall conductivity in topological Dirac semimetal Ta3As family. Npj Computational Materials, 2021, 7, .	3.5	14
74	Role of spin-lattice coupling in ultrafast demagnetization and all optical helicity-independent single-shot switching in $Gd_{1-x}Mn_x$ alloys. Physical Review B, 2022, 105, .	1.1	14
75	Short-Wavelength Spin Waves in Yttrium Iron Garnet Micro-Channels on Silicon. IEEE Magnetism Letters, 2016, 7, 1-4.	0.6	13
76	Ultrabroadband spin-wave propagation in $Co_{1-x}Mn_x$ thin films. Physical Review B, 2017, 96, .	1.2	13
77	Skyrmion-Induced Memristive Magnetic Tunnel Junction for Ternary Neural Network. IEEE Journal of the Electron Devices Society, 2019, 7, 529-533.	1.2	13
78	Modulation of thermal stability and spin-orbit torque in IrMn/CoFeB/MgO structures through atom thick W insertion. Applied Physics Letters, 2020, 117, .	1.5	13
79	Weak Kondo effect in the monocrystalline transition metal dichalcogenide $ZrTe_5$. Physical Review B, 2021, 103, .	1.1	13
80	Spintronic Computing-in-Memory Architecture Based on Voltage-Controlled Spin-Orbit Torque Devices for Binary Neural Networks. IEEE Transactions on Electron Devices, 2021, 68, 4944-4950.	1.6	13
81	Manipulating density of magnetic skyrmions via multilayer repetition and thermal annealing. Physical Review B, 2021, 104, .	1.1	12
82	Modeling for Spin-FET and Design of Spin-FET-Based Logic Gates. IEEE Transactions on Magnetism, 2017, 53, 1-6.	1.2	11
83	Extrinsic pinning of magnetic domain walls in CoFeB-MgO nanowires with perpendicular anisotropy. AIP Advances, 2018, 8, .	0.6	11
84	Radiation-Hardening Techniques for Spin Orbit Torque-MRAM Peripheral Circuitry. IEEE Transactions on Magnetism, 2018, 54, 1-5.	1.2	11
85	CORN: In-Buffer Computing for Binary Neural Network. , 2019, , .		11
86	Observation of magnetic droplets in magnetic tunnel junctions. Science China: Physics, Mechanics and Astronomy, 2022, 65, .	2.0	11
87	Addressing Failure and Aging Degradation in MRAM/MeRAM-on-FDSOI Integration. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 239-250.	3.5	10
88	An STT-MRAM Based in Memory Architecture for Low Power Integral Computing. IEEE Transactions on Computers, 2019, 68, 617-623.	2.4	10
89	Stateful implication logic based on perpendicular magnetic tunnel junctions. Science China Information Sciences, 2022, 65, 1.	2.7	10
90	Flexible Control of Broadband Polarization in a Spintronic Terahertz Emitter Integrated with Liquid Crystal and Metasurface. ACS Applied Materials & Interfaces, 2022, 14, 32646-32656.	4.0	10

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91	Spin manipulation by giant valley-Zeeman spin-orbit field in atom-thick WSe ₂ . Applied Physics Reviews, 2022, 9, .	5.5	10
92	A High-Speed Robust NVM-TCAM Design Using Body Bias Feedback. , 2015, , .		9
93	Pseudo-Differential Sensing Framework for STT-MRAM: A Cross-Layer Perspective. IEEE Transactions on Computers, 2017, 66, 531-544.	2.4	9
94	Negative Capacitance Enhanced All Spin Logic Devices With an Ultra-Low 1 mV Working Voltage. IEEE Journal of the Electron Devices Society, 2018, 6, 245-249.	1.2	9
95	Effects of Gamma Irradiation on Magnetic Properties of Double-Interface CoFeB/MgO Multilayers. IEEE Transactions on Nuclear Science, 2019, 66, 77-81.	1.2	9
96	Toward Energy-Efficient STT-MRAM Design With Multi-Modes Reconfiguration. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2633-2639.	2.2	9
97	Promising spin caloritronics and spin diode effects based on 1T-FeCl ₂ nanotube devices. Journal of Materials Chemistry C, 2022, 10, 607-615.	2.7	9
98	Anomalous Thermal-Assisted Spin-Orbit Torque-Induced Magnetization Switching for Energy-Efficient Logic-in-Memory. ACS Nano, 2022, 16, 8264-8272.	7.3	9
99	Alleviating Through-Silicon-Via Electromigration for 3-D Integrated Circuits Taking Advantage of Self-Healing Effect. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 3310-3322.	2.1	8
100	Controlled Switching of the Number of Skyrmions in a Magnetic Nanodot by Electric Fields (Adv.) Tj ETQq0 0 0 rgBT /Overlock 10 Tf 50 3	11.1	8
101	A Fast and Power-Efficient Hardware Architecture for Visual Feature Detection in Affine-SIFT. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3362-3375.	3.5	7
102	Process Variation-Resilient STT-MTJ based TRNG using Linear Correcting Codes. , 2019, , .		7
103	Field-Free Deterministic Magnetization Switching Induced by Interlaced Spin-Orbit Torques. ACS Applied Materials & Interfaces, 2021, 13, 20763-20769.	4.0	7
104	Brief Industry Paper: optimizing Memory Efficiency of Graph Neural Networks on Edge Computing Platforms. , 2021, , .		7
105	Fully Single Event Double Node Upset Tolerant Design for Magnetic Random Access Memory. , 2021, , .		7
106	A Reconfigurable Arbiter MPUF With High Resistance Against Machine Learning Attack. IEEE Transactions on Magnetics, 2021, 57, 1-7.	1.2	7
107	Magnetization Dynamics Modulated by Dzyaloshinskii-Moriya Interaction in the Double-Interface Spin-Transfer Torque Magnetic Tunnel Junction. Nanoscale Research Letters, 2019, 14, 315.	3.1	7
108	Phase-Change Controlled Magnetic Tunnel Junction for Multifunctional In-Sensor Computing. IEEE Electron Device Letters, 2022, 43, 482-485.	2.2	7

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109	High On/Off Ratio Spintronic Multi-Level Memory Unit for Deep Neural Network. <i>Advanced Science</i> , 2022, 9, e2103357.	5.6	7
110	Novel Magnetic Tunneling Junction Memory Cell With Negative Capacitance-Amplified Voltage-Controlled Magnetic Anisotropy Effect. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 4919-4927.	1.6	6
111	A spin orbit torque based true random number generator with real-time optimization. , 2018, , .		6
112	Enhancement of Perpendicular Magnetic Anisotropy Through Fe Insertion at the CoFe/W Interface. <i>IEEE Transactions on Magnetics</i> , 2018, 54, 1-5.	1.2	6
113	Reliability Analysis and Performance Evaluation of STT-MRAM-Based Physical Unclonable Function. <i>Spin</i> , 2020, 10, .	0.6	6
114	SpinLiM: Spin Orbit Torque Memory for Ternary Neural Networks Based on the Logic-in-Memory Architecture. , 2021, , .		6
115	Ultrafast and Energy-Efficient Ferrimagnetic XNOR Logic Gates for Binary Neural Networks. <i>IEEE Electron Device Letters</i> , 2021, 42, 621-624.	2.2	6
116	A Machine Learning Attack-Resilient Strong PUF Leveraging the Process Variation of MRAM. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2022, 69, 2712-2716.	2.2	6
117	Time Division Multiplexing Ising Computer Using Single Tunable True Random Number Generator Based on Spin Torque Nano-Oscillator. , 2021, , .		6
118	Theoretical Conditions for Field-Free Magnetization Switching Induced by Spin-Orbit Torque and Dzyaloshinskii-Moriya Interaction. <i>IEEE Electron Device Letters</i> , 2021, 42, 148-151.	2.2	5
119	Ionization and Displacement Damage on Nanostructure of Spin-Orbit Torque Magnetic Tunnel Junction. <i>IEEE Transactions on Nuclear Science</i> , 2022, 69, 43-49.	1.2	5
120	A Spintronic In-Memory Computing Network for Efficient Hamming Codec Implementation. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2022, 69, 2086-2090.	2.2	5
121	Accelerating Graph-Connected Component Computation With Emerging Processing-In-Memory Architecture. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , 2022, 41, 5333-5342.	1.9	5
122	Nonvolatile radiation hardened DICE latch. , 2015, , .		4
123	An architecture-level cache simulation framework supporting advanced PMA STT-MRAM. , 2015, , .		4
124	High Tunnel Magnetoresistance in Mo/CoFe/MgO Magnetic Tunnel Junction: A First-Principles Study. <i>IEEE Transactions on Magnetics</i> , 2017, 53, 1-4.	1.2	4
125	Performance Evaluation and Optimization of Single Layer MoS ₂ Double Gate Transistors With Schottky Barrier Contacts. <i>IEEE Transactions on Electron Devices</i> , 2017, 64, 2999-3006.	1.6	4
126	Unraveling the Characteristic Shape for Magnetic Field Effects in Polymer-Fullerene Solar Cells. <i>ACS Omega</i> , 2017, 2, 7777-7783.	1.6	4

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127	Interface control of domain wall depinning field. AIP Advances, 2018, 8, .	0.6	4
128	Compact Model for Negative Capacitance Enhanced Spintronics Devices. IEEE Transactions on Electron Devices, 2019, 66, 2795-2801.	1.6	4
129	Anisotropic bilinear magnetoresistance in (110) $\text{O}_3/\text{SrTiO}_3$ -based two-dimensional electron gas. Physical Review B, 2021, 104, .	1.1	4
130	Reconfigurable processing in memory architecture based on spin orbit torque. , 2017, , .		3
131	A Novel 15T-4MTJ based Non-volatile Ternary Content-Addressable Memory Cell for High-Speed, Low-Power and High-Reliable Search Operation. , 2018, , .		3
132	Advanced Spin Orbit Torque Magnetic Random Access Memory with Field-Free Switching Schemes (Invited). , 2020, , .		3
133	A Computing-in-memory Scheme with Series Bit-cell in STT-MRAM for Efficient Multi-bit Analog Multiplication. , 2021, , .		3
134	Computational Study for Spin-orbit Torque Magnetic Random Access Memory. , 2021, , .		3
135	A Mini Tutorial of Processing in Memory: From Principles, Devices to Prototypes. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3044-3050.	2.2	3
136	Spin wave based synapse and neuron for ultra low power neuromorphic computation system. , 2016, , .		2
137	Interfacial property tuning of heavy metal/CoFeB for large density STT-MRAM. , 2017, , .		2
138	Design and Data Management for Magnetic Racetrack Memory. , 2018, , .		2
139	Write Energy Optimization for STT-MRAM Cache with Data Pattern Characterization. , 2018, , .		2
140	Towards Spintronics Nonvolatile Caches. Springer Series in Advanced Microelectronics, 2020, , 1-28.	0.3	2
141	Proposal of High Density Two-Bits-Cell Based NAND-Like Magnetic Random Access Memory. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1665-1669.	2.2	2
142	Experimental Demonstration of Angle-Dependent GMR Effect in $\text{Py}/\text{WSe}_2/\text{Co}$ Spin Valve Structure. IEEE Transactions on Electron Devices, 2021, 68, 3690-3695.	1.6	2
143	Variability Study of Toggle Spin Torques Magnetic Random Access Memory. IEEE Transactions on Magnetics, 2021, 57, 1-5.	1.2	2
144	Femtosecond laser-assisted switching in perpendicular magnetic tunnel junctions with double-interface free layer. Science China Information Sciences, 2022, 65, 1.	2.7	2

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145	Generation and Control of Terahertz Spin Currents in Topology-Induced 2D Ferromagnetic Fe ₃ GeTe ₂ Bi ₂ Te ₃ Heterostructures (Adv. Mater.) Tj ETQq1 1 0.784314 2gBT /Over	1.7	14
146	Multiple modes of perpendicular magnetization switching scheme in single spin-orbit torque device. Chinese Physics B, 2022, 31, 107501.	0.7	2
147	Realization of neural coding by stochastic switching of magnetic tunnel junction. , 2015, , .		1
148	Thermosiphon: A thermal aware NUCA architecture for write energy reduction of the STT-MRAM based LLCs. , 2017, , .		1
149	Proposal for novel magnetic memory device with spin momentum locking materials. , 2017, , .		1
150	A Computing Efficient Hardware Architecture for Sparse Deep Neural Network Computing. , 2018, , .		1
151	All Perpendicular Spin Nano-Oscillators with Composite Free Layer. Spin, 2019, 09, 1940010.	0.6	1
152	Low-Power, High-Speed and High-Density Magnetic Non-Volatile SRAM Design with Voltage-Gated Spin-Orbit Torque. , 2019, , .		1
153	Fast Tunable Biological Fluorescence Detection Device with Integrable Liquid Crystal Filter. Crystals, 2021, 11, 272.	1.0	1
154	Magnetic Random-Access Memory-Based Approximate Computing: An overview. IEEE Nanotechnology Magazine, 2022, 16, 25-32.	0.9	1
155	An All-Electric Neural Device and Network Based on Laterally Coupled Nanomagnets for Binary Image Recognitions. IEEE Transactions on Electron Devices, 2022, 69, 3130-3134.	1.6	1
156	Time-Division Multiplexing Ising Computer Using Single Stochastic Magnetic Tunneling Junction. IEEE Transactions on Electron Devices, 2022, 69, 4700-4707.	1.6	1
157	Recent progresses of STT memory design and applications. , 2015, , .		0
158	Guest Editorial for Special Issue on Emerging Memory Technologies—Modeling, Design, and Applications for Multi-Scale Computing. IEEE Transactions on Multi-Scale Computing Systems, 2015, 1, 125-126.	2.5	0
159	Performance evaluation and optimization of single layer MoS ₂ double gate transistors with metallic contacts. , 2016, , .		0
160	Frequency modulation of spin torque nano oscillator with voltage controlled magnetic anisotropy effect. , 2017, , .		0
161	NEAR: A Novel Energy Aware Replacement Policy for STT-MRAM LLCs. , 2018, , .		0
162	Unconventional applications of skyrmions. , 2021, , 393-416.		0

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163	Multi-order Nonlinearities and Resulting Coherent Oscillations of the States in Quantum Dot-Mechanical Resonator Hybrid System. , 2021, , .		0
164	Optimal Design of DDR3 STT-MRAM Memory. , 2021, , .		0
165	A Novel Multi-Context Non-Volatile Content-Addressable Memory Cell and Multi-Level Architecture for High Reliability and Density. , 2021, , .		0
166	Optimized LRU Algorithm for STT-MRAM/SRAM Hybrid Cache Architecture. , 2021, , .		0
167	Robust Mobility Enhancement and Comprehensive Reliability Evaluation for Amorphous InGaZnO TFT by Double Layers With Quantum Well Structures. IEEE Transactions on Electron Devices, 2022, 69, 1876-1882.	1.6	0
168	Radiation Hardened Design of STT-MRAM with High Recoverability from Double Node Upset. , 2021, , .		0