

# Utkarsh Saxena

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	Towards ADC-Less Compute-In-Memory Accelerators for Energy Efficient Deep Learning. , 2022, , .		5
2	A 35.5-127.2 TOPS/W Dynamic Sparsity-Aware Reconfigurable-Precision Compute-in-Memory SRAM Macro for Machine Learning. IEEE Solid-State Circuits Letters, 2021, 4, 129-132.	2.0	14
3	Revisiting Stochastic Computing in the Era of Nanoscale Nonvolatile Technologies. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 2481-2494.	3.1	8
4	On-chip learning for domain wall synapse based Fully Connected Neural Network. Journal of Magnetism and Magnetic Materials, 2019, 489, 165434.	2.3	34
5	On-chip Learning In A Conventional Silicon MOSFET Based Analog Hardware Neural Network. , 2019, , .		6
6	Skyrmionic implementation of Spike Time Dependent Plasticity (STDP) enabled Spiking Neural Network (SNN) under supervised learning scheme. , 2018, , .		2
7	Low-Energy Implementation of Feed-Forward Neural Network With Back-Propagation Algorithm Using a Spin-Orbit Torque Driven Skyrmionic Device. IEEE Transactions on Magnetics, 2018, 54, 1-5.	2.1	15