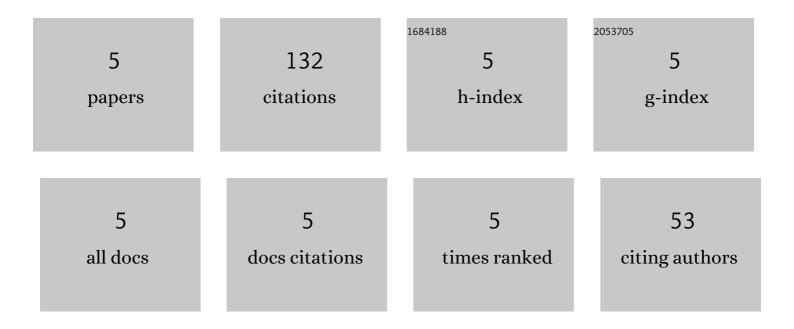
Ali KilinÇ

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/10784417/publications.pdf Version: 2024-02-01



ΔιιΚιινιδτ

#	Article	IF	CITATIONS
1	Design of Practical Matching Networks With Lumped Elements Via Modeling. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 1829-1837.	0.1	33
2	Immitance data modelling via linear interpolation techniques: A classical circuit theory approach. International Journal of Circuit Theory and Applications, 2004, 32, 537-563.	2.0	30
3	High Precision LC Ladder Synthesis Part I: Lowpass Ladder Synthesis via Parametric Approach. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2074-2083.	5.4	28
4	High Precision LC Ladder Synthesis Part II: Immittance Synthesis With Transmission Zeros at DC and Infinity. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 2719-2729.	5.4	26
5	An Immitance Based Tool for Modelling Passive One-Port Devices by Means of Darlington Equivalents. AEU - International Journal of Electronics and Communications, 2001, 55, 443-451.	2.9	15