Masahiko Yoshimoto

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/10766760/publications.pdf

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29 papers 186

6 h-index

10 g-index

29 all docs 29 docs citations

29 times ranked 214 citing authors

#	Article	IF	CITATIONS
1	Recent Progress of Biomedical Processor SoC for Wearable Healthcare Application: A Review. IEICE Transactions on Electronics, 2019, E102.C, 245-259.	0.3	10
2	28-nm FD-SOI Dual-Port SRAM with MSB-Based Inversion Logic for Low-Power Deep Learning. , 2018, , .		O
3	A 11.3-µA Physical Activity Monitoring System Using Acceleration and Heart Rate. IEICE Transactions on Electronics, 2018, E101.C, 233-242.	0.3	1
4	Estimating metabolic equivalents for activities in daily life using acceleration and heart rate in wearable devices. BioMedical Engineering OnLine, 2018, 17, 100.	1.3	14
5	A metabolic equivalents estimation algorithm using triaxial accelerometer and adaptive sampling for wearable devices. , 2017, , .		1
6	A 28-nm 484-fJ/writecycle 650-fJ/readcycle 8T Three-Port FD-SOI SRAM for Image Processor. IEICE Transactions on Electronics, 2016, E99.C, 901-908.	0.3	O
7	A $15\hat{-}1^1\!\!/\!\!4$ A metabolic equivalents monitoring system using adaptive acceleration sampling and normally off computing. , 2016, , .		3
8	Normally Off ECG SoC With Non-Volatile MCU and Noise Tolerant Heartbeat Detector. IEEE Transactions on Biomedical Circuits and Systems, 2015, 9, 641-651.	2.7	35
9	An FPGA Implementation of a HOG-based Object Detection Processor. IPSJ Transactions on System LSI Design Methodology, 2013, 6, 42-51.	0.5	11
10	A 40 nm 144 mW VLSI Processor for Real-Time 60-kWord Continuous Speech Recognition. IEEE Transactions on Circuits and Systems I: Regular Papers, 2012, 59, 1656-1666.	3.5	8
11	A 40 nm 144 mW VLSI processor for realtime 60 kWord continuous speech recognition. , 2011, , .		8
12	Data-Intensive Sound Acquisition System with Large-scale Microphone Array. Journal of Information Processing, 2011, 19, 129-140.	0.3	1
13	VLSI Architecture of GMM Processing and Viterbi Decoder for 60,000-Word Real-Time Continuous Speech Recognition. IEICE Transactions on Electronics, 2011, E94-C, 458-467.	0.3	6
14	A Low-Power Multi Resolution Spectrum Sensing Architecture for a Wireless Sensor Network with Cognitive Radio. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2011, E94-A, 2287-2294.	0.2	1
15	A power-variation model for sensor node and the impact against life time of wireless sensor networks. IEICE Electronics Express, 2010, 7, 197-202.	0.3	O
16	Live demonstration: Intelligent ubiquitous sensor network for sound acquisition. , 2010, , .		3
17	Intelligent ubiquitous sensor network for sound acquisition. , 2010, , .		3
18	Microphone array network for ubiquitous sound acquisition. , 2010, , .		8

#	Article	IF	CITATIONS
19	A Low-Power Multi Resolution Spectrum Sensing (MRSS) Architecture for a Wireless Sensor Network with Cognitive Radio. , $2010, , .$		8
20	Low-Traffic and Low-Power Data-Intensive Sound Acquisition with Perfect Aggregation Specialized for Microphone Array Networks. , 2010, , .		3
21	A 7T/14T Dependable SRAM and its Array Structure to Avoid Half Selection. , 2009, , .		19
22	Novel Video Memory Reduces 45% of Bitline Power Using Majority Logic and Data-Bit Reordering. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2008, 16, 620-627.	2.1	22
23	An inter-die variability compensation scheme for 0.42-V 486-kb FD-SOI SRAM using substrate control. , 2008, , .		5
24	Hop count aware broadcast algorithm with Random Assessment Delay extension for wireless sensor networks. , 2008, , .		1
25	Multipath Routing using Isochronous Medium Access Control with Multi Wakeup Period for Wireless Sensor Networks. , 2007, , .		0
26	Power and Memory Bandwidth Reduction of an H.264/AVC HDTV Decoder LSI with Elastic Pipeline Architecture. , 2007, , .		2
27	Improvement of Counter-based Broadcasting by Random Assessment Delay Extension for Wireless Sensor Networks. , 2007, , .		6
28	A VGA 30-fps optical-flow processor core based on Pyramidal Lucas and Kanade algorithm., 2007,,.		1
29	A Power-Variation Model for Sensor Node and the Impact against Life Time of Wireless Sensor Networks. , 2006, , .		6