Johnny Ã–berg

List of Publications by Year
in descending order

		2258059	2272923
10 papers	**57** citations	**3** h-index	**4** g-index
10 all docs	**10** docs citations	**10** times ranked	**37** citing authors

#	Article	IF	Citations
1	SAFEPOWER project: Architecture for safe and power-efficient mixed-criticality systems. Microprocessors and Microsystems, 2017, 52, 89-105.	2.8	17
2	System Level Virtual Prototyping of DSP SOCs Using Grammar Based Approach. Design Automation for Embedded Systems, 2000, 5, 295-311.	1.0	11
3	Exploring Power and Throughput for Dataflow Applications on Predictable NoC Multiprocessors. , 2018, , .		8
4	Artificial neural network emulation on NOC based multi-core FPGA platform. , 2012, , .		7
5	Grammar-based design of embedded systems. Journal of Systems Architecture, 2001, 47, 225-240.	4.3	6
6	Implementation of a scalable, globally plesiochronous locally synchronous, off-chip NoC communication protocol. , 2009, , .		3
7	Towards cognitive reconfigurable hardware: Self-aware learning in RTR fault-tolerant SoCs. , 2015, , .		2
8	Experimental Evaluation of SAFEPOWER Architecture for Safe and Power-Efficient Mixed-Criticality Systems. Journal of Low Power Electronics and Applications, 2019, 9, 12.	2.0	2
9	A formal, model-driven design flow for system simulation and multi-core implementation. , 2015, , .		1
10	Synthesis of VLIW Accelerators from Formal Descriptions in a Real-Time Multi-Core Environment. , 2017, , .		0