

# I-Chung Chiu

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10719530/publications.pdf>

Version: 2024-02-01

12

papers

204

citations

1478505

6

h-index

1372567

10

g-index

12

all docs

12

docs citations

12

times ranked

258

citing authors

#	ARTICLE	IF	CITATIONS
1	Gate-Bias Stress Stability of P-Type SnO Thin-Film Transistors Fabricated by RF-Sputtering. <i>IEEE Electron Device Letters</i> , 2014, 35, 90-92.	3.9	63
2	Complementary Oxide Semiconductor-Based Circuits With n-Channel ZnO and p-Channel SnO Thin-Film Transistors. <i>IEEE Electron Device Letters</i> , 2014, 35, 1263-1265.	3.9	59
3	Influence of rapid-thermal-annealing temperature on properties of rf-sputtered SnO <sub>x</sub> thin films. <i>Applied Surface Science</i> , 2015, 327, 358-363.	6.1	27
4	Flexible Transparent ZnO:Al/ZnO/CuAlO <sub>x</sub> :Ca Heterojunction Diodes on Polyethylene Terephthalate Substrates. <i>Journal of Electronic Materials</i> , 2013, 42, 1242-1245.	2.2	24
5	Two dimensional thermoelectric platforms for thermocapillary droplet actuation. <i>RSC Advances</i> , 2012, 2, 1639-1642.	3.6	11
6	Optoelectronic properties of infrared rapid-thermal-annealed SnO <sub>x</sub> thin films. <i>Ceramics International</i> , 2015, 41, 13502-13508.	4.8	8
7	Nitrogen Atmospheric-Pressure-Plasma-Jet Induced Oxidation of SnO <sub>x</sub> Thin Films. <i>Plasma Chemistry and Plasma Processing</i> , 2015, 35, 979-991.	2.4	5
8	Electromechanical Stability of Flexible Nanocrystalline-Silicon Thin-Film Transistors. <i>IEEE Electron Device Letters</i> , 2010, 31, 222-224.	3.9	4
9	Oxidation of sputtered metallic Sn thin films using N <sub>2</sub> atmospheric pressure plasma jets. <i>Materials Research Express</i> , 2015, 2, 016504.	1.6	2
10	Effects of electro-mechanical stressing on the electrical characterization of on-plastic a-Si:H thin film transistors. <i>Materials Research Society Symposia Proceedings</i> , 2009, 1153, 1.	0.1	1
11	DC and AC Gate-Bias Stability of Nanocrystalline Silicon Thin-Film Transistors Made on Colorless Polyimide Foil Substrates. <i>Materials Research Society Symposia Proceedings</i> , 2011, 1321, 259.	0.1	0
12	Enhancement of gate-bias and current stress stability of P-type SnO thin-film transistors with SiN <sub>x</sub> /HfO <sub>2</sub> passivation layers. , 2016, , .	0	0