

Piotr Katarzyński

List of Publications by Year in descending order

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Version: 2024-02-01

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papers

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2148532

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5
docs citations

5
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26
citing authors

#	ARTICLE	IF	CITATIONS
1	SI-Studio, a layout generator of current mode circuits. Expert Systems With Applications, 2015, 42, 3205-3218.	4.4	15
2	Analog filter pair design on the basis of a gyrator-capacitor prototype circuit. International Journal of Circuit Theory and Applications, 2012, 40, 539-550.	1.3	12
3	Design automation of a lossless multiport network and its application to image filtering. Expert Systems With Applications, 2014, 41, 2211-2221.	4.4	9
4	VHDL-CAMS in switched-current analog filter pair design based on a gyrator-capacitor prototype circuit. International Journal of Numerical Modelling: Electronic Networks, Devices and Fields, 2014, 27, 268-281.	1.2	7
5	Automated analog filter pair design on the basis of a gyrator-capacitor prototype circuit realised in SI technique. , 2012, , .		1