

Edward W Felten

List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

18
papers

910
citations

10
h-index

18
g-index

18
ext. papers

1,111
ext. citations

1.4
avg, IF

3.51
L-index

#	Paper	IF	Citations
18	The Variable Impact of Artificial Intelligence on Labor: The Role of Complementary Skills and Technologies. <i>SSRN Electronic Journal</i> , 2019 ,	1	13
17	A Method to Link Advances in Artificial Intelligence to Occupational Abilities. <i>AEA Papers and Proceedings American Economic Association</i> , 2018 , 108, 54-57	1.6	26
16	"You Might Also Like:" Privacy Risks of Collaborative Filtering 2011 ,		148
15	Lest we remember. <i>Communications of the ACM</i> , 2009 , 52, 91-98	2.5	470
14	Fingerprinting Blank Paper Using Commodity Scanners 2009 ,		34
13	Coping with Outside-the-Box Attacks. <i>Lecture Notes in Computer Science</i> , 2008 , 3-4	0.9	
12	Mechanisms for secure modular programming in Java. <i>Software - Practice and Experience</i> , 2003 , 33, 461-480	1.9	10
11	Statically Scanning Java Code: Finding Security Vulnerabilities. <i>IEEE Software</i> , 2000 , 17, 68-74	1.5	20
10	Performance measurements for multithreaded programs. <i>Performance Evaluation Review</i> , 1998 , 26, 161-170	1.7	6
9	Design choices in the SHRIMP system. <i>Computer Architecture News</i> , 1998 , 26, 330-341		3
8	Extensible security architectures for Java. <i>Operating Systems Review (ACM)</i> , 1997 , 31, 116-128	0.8	7
7	Fast RPC on the SHRIMP Virtual Memory Mapped Network Interface. <i>Journal of Parallel and Distributed Computing</i> , 1997 , 40, 138-146	4.4	11
6	Early experience with message-passing on the SHRIMP multicomputer. <i>Computer Architecture News</i> , 1996 , 24, 296-307		6
5	Early experience with message-passing on the SHRIMP multicomputer 1996 ,		15
4	Integrated parallel prefetching and caching. <i>Performance Evaluation Review</i> , 1996 , 24, 262-263	0.4	4
3	A study of integrated prefetching and caching strategies 1995 ,		105
2	Evaluating multi-port frame buffer designs for a mesh-connected multicomputer. <i>Computer Architecture News</i> , 1995 , 23, 96-105		

- 1 A study of integrated prefetching and caching strategies. *Performance Evaluation Review*, **1995**, 23, 188-197 32