

# Edward W Felten

## List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

18  
papers

910  
citations

10  
h-index

18  
g-index

18  
ext. papers

1,111  
ext. citations

1.4  
avg, IF

3.51  
L-index

#	Paper	IF	Citations
18	Lest we remember. <i>Communications of the ACM</i> , <b>2009</b> , 52, 91-98	2.5	470
17	"You Might Also Like:" Privacy Risks of Collaborative Filtering <b>2011</b> ,		148
16	A study of integrated prefetching and caching strategies <b>1995</b> ,		105
15	Fingerprinting Blank Paper Using Commodity Scanners <b>2009</b> ,		34
14	A study of integrated prefetching and caching strategies. <i>Performance Evaluation Review</i> , <b>1995</b> , 23, 188-197	1.7	32
13	A Method to Link Advances in Artificial Intelligence to Occupational Abilities. <i>AEA Papers and Proceedings American Economic Association</i> , <b>2018</b> , 108, 54-57	1.6	26
12	Statically Scanning Java Code: Finding Security Vulnerabilities. <i>IEEE Software</i> , <b>2000</b> , 17, 68-74	1.5	20
11	Early experience with message-passing on the SHRIMP multicomputer <b>1996</b> ,		15
10	The Variable Impact of Artificial Intelligence on Labor: The Role of Complementary Skills and Technologies. <i>SSRN Electronic Journal</i> , <b>2019</b> ,	1	13
9	Fast RPC on the SHRIMP Virtual Memory Mapped Network Interface. <i>Journal of Parallel and Distributed Computing</i> , <b>1997</b> , 40, 138-146	4.4	11
8	Mechanisms for secure modular programming in Java. <i>Software - Practice and Experience</i> , <b>2003</b> , 33, 461-489	1.9	10
7	Extensible security architectures for Java. <i>Operating Systems Review (ACM)</i> , <b>1997</b> , 31, 116-128	0.8	7
6	Early experience with message-passing on the SHRIMP multicomputer. <i>Computer Architecture News</i> , <b>1996</b> , 24, 296-307		6
5	Performance measurements for multithreaded programs. <i>Performance Evaluation Review</i> , <b>1998</b> , 26, 161-170	1.7	6
4	Integrated parallel prefetching and caching. <i>Performance Evaluation Review</i> , <b>1996</b> , 24, 262-263	0.4	4
3	Design choices in the SHRIMP system. <i>Computer Architecture News</i> , <b>1998</b> , 26, 330-341		3
2	Evaluating multi-port frame buffer designs for a mesh-connected multicomputer. <i>Computer Architecture News</i> , <b>1995</b> , 23, 96-105		

1 Coping with Outside-the-Box Attacks. *Lecture Notes in Computer Science*, **2008**, 3-4

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