

# Jamshid Sangirov

## List of Publications by Year in descending order

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16  
papers

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citations

2258059

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1872680

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docs citations

16  
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#	ARTICLE	IF	CITATIONS
1	40 Gb/s optical subassembly module for a multi-channel bidirectional optical link. Optics Express, 2014, 22, 1768.	3.4	12
2	10 Gbps Transimpedance Amplifier-Receiver for Optical Interconnects. Journal of the Optical Society of Korea, 2013, 17, 44-49.	0.6	9
3	Design and fabrication of a 10 Gbps transimpedance amplifier-receiver for optical interconnects. Journal of Computational Electronics, 2015, 14, 669-674.	2.5	4
4	Signal Synchronization Using a Flicker Reduction and Denoising Algorithm for Video-Signal Optical Interconnect. ETRI Journal, 2012, 34, 122-125.	2.0	3
5	0.013 mm <sup>2</sup> and Low-power 10 Gb/s Transimpedance Amplifier for Short-reach Optical Interconnects. Microwave and Optical Technology Letters, 2013, 55, 2484-2487.	1.4	2
6	Analysis of thermal effects on crosstalk and performance of optoelectronic transmitter modules. , 2012, , .		1
7	Short turn-on/off time linear voltage regulator with data detector for power-aware optical interconnect system. , 2012, , .		1
8	Power saving bidirectional optical transceiver design and fabrication. Optical and Quantum Electronics, 2015, 47, 3101-3116.	3.3	1
9	Thermal effect analysis on crosstalk and performance of optoelectronic transmitter modules for optical interconnects. Optical and Quantum Electronics, 2017, 49, 1.	3.3	1
10	Ultra-low power TIA with variable bandwidth in 0.13 $\mu$ m CMOS for short-range optical interconnects. Journal of Engineering, 2021, 2021, 295-300.	1.1	1
11	Signal Synchronization Using a Flicker Reduction and Denoising Algorithm for Video-Signal Optical Interconnect. ETRI Journal, 2012, 34, 122-125.	2.0	1
12	Power-aware transceiver design for half-duplex bidirectional chip-to-chip optical interconnects. Journal of Semiconductors, 2013, 34, 125001.	3.7	0
13	Power Saving Rx Design for Optical Modules. Journal of Circuits, Systems and Computers, 2015, 24, 1550095.	1.5	0
14	Optical transceiver with in-chip temperature compensation module design and fabrication. Optical and Quantum Electronics, 2016, 48, 1.	3.3	0
15	Design and analysis of a multichannel transceiver for high-speed optical interconnects. Optical and Quantum Electronics, 2016, 48, 1.	3.3	0
16	Design of full-duplex and multifunction bidirectional CMOS transceiver for optical interconnect applications. Optical and Quantum Electronics, 2017, 49, 1.	3.3	0