Jaejin Lee

List of Publications by Year
in descending order

17 papers	446 citations	1163117 8 h-index	1588992 8 g-index
17 all docs	17 docs citations	17 times ranked	356 citing authors

#	Article	IF	Citations
1	Dimensionality reduction and unsupervised clustering for EELS-SI. Ultramicroscopy, 2021, 231, 113314.	1.9	9
2	SOFF: An OpenCL High-Level Synthesis Framework for FPGAs. , 2020, , .		9
3	Translating OpenMP Device Constructs to OpenCL Using Unnecessary Data Transfer Elimination. , 2016, , .		6
4	Efficient Checkpointing of Live Virtual Machines. IEEE Transactions on Computers, 2016, 65, 3041-3054.	3.4	8
5	Accelerating LINPACK with MPI-OpenCL on Clusters of Multi-GPU Nodes. IEEE Transactions on Parallel and Distributed Systems, 2015, 26, 1814-1825.	5.6	26
6	A Performance Model for GPUs with Caches. IEEE Transactions on Parallel and Distributed Systems, 2015, 26, 1800-1813.	5.6	25
7	An OpenCL optimizing compiler for reconfigurable processors. , 2013, , .		0
8	An OpenCL Framework for Homogeneous Manycores with No Hardware Cache Coherence. , 2011, , .		9
9	SFMalloc: A Lock-Free and Mostly Synchronization-Free Dynamic Memory Allocator for Manycores. , 2011, , .		26
10	Performance characterization of the NAS Parallel Benchmarks in OpenCL. , 2011, , .		152
11	A Software-Managed Coherent Memory Architecture for Manycores. , 2011, , .		3
12	COMIC++: A software SVM system for heterogeneous multicore accelerator clusters. , 2010, , .		0
13	Hydra: A Block-Mapped Parallel Flash Memory Solid-State Disk Architecture. IEEE Transactions on Computers, 2010, 59, 905-921.	3.4	80
14	Scratchpad Memory Management Techniques for Code in Embedded Systems without an MMU. IEEE Transactions on Computers, 2010, 59, 1047-1062.	3.4	24
15	Prefetching with Helper Threads for Loosely Coupled Multiprocessor Systems. IEEE Transactions on Parallel and Distributed Systems, 2009, 20, 1309-1324.	5.6	29
16	Design and implementation of software-managed caches for multicores with local memory. , 2009, , .		26
17	Eliminating Conflict Misses Using Prime Number-Based Cache Indexing. IEEE Transactions on Computers, 2005, 54, 573-586.	3.4	14