

Oliver Rose

List of Publications by Year in descending order

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Version: 2024-02-01

50
papers

833
citations

2148532

4
h-index

2501632

4
g-index

51
all docs

51
docs citations

51
times ranked

598
citing authors

#	ARTICLE	IF	CITATIONS
1	A Global Wip Oriented Dispatching Scheme: Work-Center Workload Balance Without Relying on Target Wip. , 2019, , .		0
2	SIMULATION-BASED AUTONOMOUS ALGORITHM SELECTION FOR DYNAMIC VEHICLE ROUTING PROBLEMS WITH THE HELP OF SUPERVISED LEARNING METHODS. , 2018, , .		1
3	PANEL: EDUCATION ON SIMULATION MODEL SIMPLIFICATION " BEYOND RULES OF THUMB. , 2018, , .		2
4	History and perspective of simulation in manufacturing. , 2017, , .		10
5	Simulation based approach to calculate utilization limits in Opto semiconductor frontends. , 2017, , .		3
6	Evaluation of modeling tools for autocorrelated input processes. , 2016, , .		2
7	Simulation and optimization of Content Delivery Networks considering user profiles and preferences of Internet Service Providers. , 2016, , .		6
8	Evaluation of the general applicability of Dragoon for the k-center problem. , 2016, , .		0
9	Impact of time bound constraints and batching on metallization in an opto-semiconductor fab. , 2016, , .		4
10	Flexible job-shop scheduling with overlapping machine sets. , 2015, , .		0
11	A novel multi-agent system for complex scheduling problems. , 2014, , .		1
12	Job release under due date constraints in job shops with time-varying product mix. , 2014, , .		0
13	JARTA — A Java library to model and fit Autoregressive-To-Anything processes. , 2013, , .		2
14	Simulation-based overhead-crane scheduling for a manufacturing plant. , 2013, , .		4
15	Study on multi-objective optimization for parallel batch machine scheduling using variable neighbourhood search. , 2013, , .		4
16	Practical assessment of a combined dispatching policy at a high-mix low-volume asic facility. , 2013, , .		0
17	Intelligent dispatching in dynamic stochastic job shops. , 2013, , .		6
18	A Simulation-Based Optimization heuristic using self-organization for complex assembly lines. , 2012, , .		6

#	ARTICLE	IF	CITATIONS
19	A free simulator for modeling production systems with SysML. , 2012, , .		1
20	Development and introduction of a combined dispatching policy at a high-mix low-volume ASIC facility. , 2012, , .		1
21	Simulation-based optimization for groups of cluster tools in semiconductor manufacturing using simulated annealing. , 2011, , .		1
22	Cluster based analytical method for the lot delivery forecast in semiconductor fab with wide product range. , 2011, , .		2
23	A prototype simulation tool for a framework for simulation-based optimization of assembly lines. , 2011, , .		4
24	A composite rule combining due date control and WIP balance in a wafer fab. , 2011, , .		6
25	A general model description for discrete processes. , 2011, , .		5
26	A survey of problems, solution techniques, and future challenges in scheduling semiconductor manufacturing operations. Journal of Scheduling, 2011, 14, 583-599.	1.3	312
27	Automated generation of analytical process time models for cluster tools in semiconductor manufacturing. , 2011, , .		4
28	A detailed model for a high-mix low-volume ASIC fab. , 2011, , .		3
29	Challenges and solution approaches for the online simulation of semiconductor wafer fabs. , 2011, , .		4
30	Framework for simulation based scheduling of assembly lines. , 2010, , .		4
31	Towards realization of a high-fidelity simulation model for short-term horizon forecasting in wafer fabrication facilities. , 2010, , .		14
32	A pull/push concept for toolgroup workload balance in wafer fab. , 2010, , .		8
33	First steps towards a general SysML model for discrete processes in production systems. , 2009, , .		31
34	Are simulation standards in our future?. , 2009, , .		4
35	Evaluation of modeling, simulation and optimization approaches for work flow management in semiconductor manufacturing. , 2009, , .		4
36	A bottleneck detection and dynamic dispatching strategy for semiconductor wafer fabrication facilities. , 2009, , .		8

#	ARTICLE	IF	CITATIONS
37	A multi-criteria approach for scheduling semiconductor wafer fabrication facilities. Journal of Scheduling, 2008, 11, 29-47.	1.3	50
38	An optimization framework for waferfab performance enhancement. , 2008, , .		2
39	Automated generation and parameterization of throughput models for semiconductor tools. , 2008, , .		4
40	Simulation analysis of semiconductor manufacturing with small lot size and batch tool replacements. , 2008, , .		5
41	A simulation based optimization algorithm for slack reduction and workforce scheduling. , 2008, , .		10
42	Queue time and x-factor characteristics for semiconductor manufacturing with small lot sizes. , 2007, , .		4
43	Improved simple simulation models for semiconductor wafer factories. , 2007, , .		31
44	Predicting cluster tool behavior with slow down factors. , 2007, , .		9
45	Implementation of a Simulation-Based Optimizer for Semiconductor Wafer Factories. , 2006, , .		4
46	Modeling Semiconductor Tools for Small Lotsize Fab Simulations. , 2006, , .		10
47	The use of Slow Down Factors for the Analysis and Development of Scheduling Algorithms for Parallel Cluster Tools. , 2006, , .		4
48	Economy of Scale Effects for Large Wafer Fabs. , 2006, , .		2
49	Scheduling and Simulation. , 2006, , 109-133.		9
50	Grand Challenges in Modeling and Simulation of Complex Manufacturing Systems. Simulation, 2004, 80, 469-476.	1.1	181