Marcelo Lubaszewski

List of Publications by Year in descending order

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		1937685	2272923
8	266	4	4
papers	citations	h-index	g-index
8	8	8	82
all docs	docs citations	times ranked	citing authors

#	Article	IF	CITATIONS
1	A High-Fault-Coverage Approach for the Test of Data, Control and Handshake Interconnects in Mesh Networks-on-Chip. IEEE Transactions on Computers, 2008, 57, 1202-1215.	3.4	72
2	Reusing an on-chip network for the test of core-based systems. ACM Transactions on Design Automation of Electronic Systems, 2004, 9, 471-499.	2.6	64
3	Improving yield of torus nocs through fault-diagnosis-and-repair of interconnect faults. , 2009, , .		33
4	Functional Test of Mesh-Based NoCs with Deterministic Routing: Integrating the Test of Interconnects and Routers. Journal of Electronic Testing: Theory and Applications (JETTA), 2011, 27, 635-646.	1.2	31
5	Concurrent test of Network-on-Chip interconnects and routers. , 2010, , .		28
6	Redefining and testing interconnect faults in Mesh NoCs. , 2007, , .		19
7	Improving the yield of NoC-based systems through fault diagnosis and adaptive routing. Journal of Parallel and Distributed Computing, 2011, 71, 664-674.	4.1	10
8	A broad strategy to detect crosstalk faults in network-on-chip interconnects. , 2010, , .		9