Huapeng Wu

List of Publications by Year
in descending order

54	818	687220	610775
papers	citations	13	24
		h-index	g-index

54	54	54	397
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	Bit-parallel finite field multiplier and squarer using polynomial basis. IEEE Transactions on Computers, 2002, 51, 750-758.	2.4	128
2	Efficient hardware implementation of the hyperbolic tangent sigmoid function. , 2009, , .		73
3	Finite field multiplier using redundant representation. IEEE Transactions on Computers, 2002, 51, 1306-1316.	2.4	66
4	High Speed VLSI Implementation of the Hyperbolic Tangent Sigmoid Function. , 2008, , .		62
5	Montgomery multiplier and squarer for a class of finite fields. IEEE Transactions on Computers, 2002, 51, 521-529.	2.4	59
6	Low complexity bit-parallel multipliers for a class of finite fields. IEEE Transactions on Computers, 1998, 47, 883-887.	2.4	48
7	Conceptual design main progress of EAST Articulated Maintenance Arm (EAMA) system. Fusion Engineering and Design, 2016, 104, 40-45.	1.0	37
8	A Word-Level Finite Field Multiplier Using Normal Basis. IEEE Transactions on Computers, 2011, 60, 890-895.	2.4	26
9	Efficient exponentiation of a primitive root in GF(2/sup m/). IEEE Transactions on Computers, 1997, 46, 162-172.	2.4	24
10	Bit-Parallel Polynomial Basis Multiplier for New Classes of Finite Fields. IEEE Transactions on Computers, 2008, 57, 1023-1031.	2.4	22
11	Development and control towards a parallel water hydraulic weld/cut robot for machining processes in ITER vacuum vessel. Fusion Engineering and Design, 2005, 75-79, 625-631.	1.0	21
12	Comb Architectures for Finite Field Multiplication in F(2^m). IEEE Transactions on Computers, 2007, 56, 909-916.	2.4	19
13	Accuracy analysis of hybrid parallel robot for the assembling of ITER. Fusion Engineering and Design, 2009, 84, 1964-1968.	1.0	16
14	Low-Power Design for a Digit-Serial Polynomial Basis Finite Field Multiplier Using Factoring Technique. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 441-449.	2.1	15
15	ANFIS and Fuzzy Tuning of PID Controller for Trajectory Tracking of a Flexible Hydraulically Driven Parallel Robot Machine. Journal of Automation and Control Engineering, 2013, 1, 70-77.	0.3	14
16	Artificial neural networks activation function HDL coder. , 2009, , .		13
17	Efficient multiplication architecture over truncated polynomial ring for NTRUEncrypt system. , 2016, , .		13
18	A New Finite-Field Multiplier Using Redundant Representation. IEEE Transactions on Computers, 2008, 57, 716-720.	2.4	12

#	Article	IF	Citations
19	Static stiffness modelling of EAST articulated maintenance arm using matrix structural analysis method. Fusion Engineering and Design, 2017, 124, 507-511.	1.0	12
20	Highly Regular Architectures for Finite Field Computation Using Redundant Basis. Lecture Notes in Computer Science, 1999, , 269-279.	1.0	12
21	Overview of the CFETR remote handling system and the development progress. Fusion Engineering and Design, 2022, 177, 113060.	1.0	12
22	Low Complexity Bit-Parallel Multiplier for a Class of Finite Fields. , 2006, , .		11
23	Closed-form expression for the average weight of signed-digit representations. IEEE Transactions on Computers, 1999, 48, 848-851.	2.4	10
24	Mechanical design and error prediction of a flexible manipulator system applied in nuclear fusion environment. Industrial Robot, 2017, 44, 711-719.	1.2	10
25	A High-Speed Word Level Finite Field Multiplier in ${BBF}_{2^m}$ Using Redundant Representation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2009, 17, 1546-1550.	2.1	9
26	Truncation Schemes for Recursive Multipliers. , 0, , .		8
27	Systematic design of snake arm maintainer in nuclear industry. Fusion Engineering and Design, 2022, 176, 113049.	1.0	8
28	Fixed-Width Multi-Level Recursive Multipliers. , 2006, , .		7
29	Efficient architecture and implementation for NTRUEncrypt system. , 2015, , .		7
30	Efficient Finite Field Processor for GF(2^163) and its VLSI Implementation. , 2007, , .		6
31	An Efficient Finite Field Multiplier Using Redundant Representation. Transactions on Embedded Computing Systems, 2012, 11, 1-14.	2.1	6
32	A high speed word level finite field multiplier using reordered normal basis. , 2008, , .		5
33	Efficient VLSI implementation of a finite field multiplier using reordered normal basis. , 2010, , .		3
34	Solid lubrication with MoS_2-Ti-C films for high-vacuum applications in a nuclear fusion experimental device. Industrial Lubrication and Tribology, 2018, 70, 155-160.	0.6	3
35	Deformation modeling of remote handling EAMA robot by recurrent neural networks. Industrial Robot, 2019, 46, 300-310.	1.2	3
36	High Speed Word-parallel Bit-Serial Normal Basis Finite Field Multiplier and Its FPGA Implementation. , 0, , .		2

#	Article	IF	Citations
37	A Parallel-In Serial-Out Multiplier Using Redundant Representation for A Class of Finite Fields. , 2006, , .		2
38	Efficient architectures for modular exponentiation using Montgomery powering ladder. , 2011, , .		2
39	Truncation Scheme for Recursive Multipliers. , 2011, , .		2
40	Fast modular reduction for large-integer multiplication for cryptosystem application. , 2012, , .		2
41	Intelligent controller of a flexible hybrid robot machine for ITER assembly and maintenance. Fusion Engineering and Design, 2014, 89, 1795-1803.	1.0	2
42	Open software architecture for east articulated maintenance arm. Fusion Engineering and Design, 2016, 109-111, 474-479.	1.0	2
43	A Serial-In Parallel-Out Multiplier Using Redundant Representation For A Class of Finite Fields. , 2006, , .		1
44	Current mode multiple-valued adder for cryptography processors. , 2012, , .		1
45	Optimization of the geometric parameters of the EAST articulated maintenance arm (EAMA) with a collision-free workspace determination in EAST. Fusion Engineering and Design, 2019, 139, 155-162.	1.0	1
46	Area-Efficient Finite Field Multiplication Using Hybrid SET-MOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 4358-4366.	3.5	1
47	Efficient realization for a class of clock-controlled sequence generators. , 0, , .		0
48	Efficient computation of multiple points for elliptic curve cryptosystems. , 0, , .		0
49	High speed VLSI implementation of a finite field multiplier using redundant representation. , 2009, , .		0
50	Public-Key Based Efficient Key Distribution in Bluetooth. , 2013, , .		0
51	Time-efficient computation of digit serial Montgomery multiplication. , 2014, , .		0
52	LFSR based low complexity montgomery multiplier in $GF(2^m)$ for a class of fields. , 2014, , .		0
53	Efficient radix conversions for classes of radices. , 2015, , .		0
54	An automated power estimation and optimization methodology. , 2015, , .		0