

# Jin-Sung Kim

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10512383/publications.pdf>

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9  
papers

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1684188

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1720034

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9  
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60  
citing authors

| # | ARTICLE   | IF  | CITATIONS |
|---|---|-----|-----------|
| 1 | A High-Throughput Hardware Design of a One-Dimensional SPIHT Algorithm. IEEE Transactions on Multimedia, 2016, 18, 392-404.   | 7.2 | 24        |
| 2 | A Block Truncation Coding Algorithm and Hardware Implementation Targeting 1/12 Compression for LCD Overdrive. Journal of Display Technology, 2016, 12, 376-389.                                   | 1.2 | 10        |
| 3 | Fixed-Ratio Compression of an RGBW Image and Its Hardware Implementation. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2016, 6, 484-496.                                 | 3.6 | 8         |
| 4 | Effect of Load Distribution on the Voltage Drop and the Luminance Variation in an AC-PDP. IEEE Electron Device Letters, 2007, 28, 896-898.  | 3.9 | 7         |
| 5 | Hierarchical Motion Estimation for Small Objects in Frame-Rate Up-Conversion. IEEE Access, 2018, 6, 60353-60360.  | 4.2 | 7         |
| 6 | Power-aware design with various low-power algorithms for an H.264/AVC encoder. , 2011, , .  |     | 6         |
| 7 | Error Propagation Algorithm for Reduction of Errors Due to Total Load and Line Load in a Plasma Panel Display. IEEE Transactions on Circuits and Systems for Video Technology, 2009, 19, 561-573. | 8.3 | 1         |
| 8 | The Effect of the Vertical Load Distribution on the Luminance of a Plasma Display Panel. IEEE Transactions on Electron Devices, 2010, 57, 1217-1223.  | 3.0 | 1         |
| 9 | Reduction of Luminance Errors Due to Line Load in a PDP With Limited Display Levels. Journal of Display Technology, 2010, 6, 65-74.   | 1.2 | 0         |