

Robert M Fox

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/10497729/publications.pdf>

Version: 2024-02-01

13
papers

276
citations

1307594

7
h-index

1474206

9
g-index

13
all docs

13
docs citations

13
times ranked

313
citing authors

#	ARTICLE	IF	CITATIONS
1	Biocompatible, High Precision, Wideband, Improved Howland Current Source With Lead-Lag Compensation. IEEE Transactions on Biomedical Circuits and Systems, 2013, 7, 63-70.	4.0	95
2	A CMOS-MEMS Gyroscope Interface Circuit Design With High Gain and Low Temperature Dependence. IEEE Sensors Journal, 2011, 11, 2740-2748.	4.7	15
3	A sub-1V CMOS voltage reference generator. , 2011, , .		1
4	Evaluation of Measurement Uncertainties Caused by Common and Cross Modes in Differential Measurements Using Baluns. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 1485-1492.	4.6	4
5	Broadband Active Balun Using Combined Cascodeâ€œCascade Configuration. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 1790-1796.	4.6	35
6	A New Characterization and Calibration Method for 3-dB-Coupled On-Wafer Measurements. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 1193-1200.	4.6	17
7	Marchand Balun Embedded Probe. IEEE Transactions on Microwave Theory and Techniques, 2008, 56, 1207-1214.	4.6	13
8	A Micromachined Dual-Backplate Capacitive Microphone for Aeroacoustic Measurements. Journal of Microelectromechanical Systems, 2007, 16, 1289-1302.	2.5	72
9	Embedded Jitter Measurement of High-speed I/O Signals. , 2007, , .		0
10	Integrated BiCMOS 10 GHz S-Parameter Module. Bipolar/BiCMOS Circuits and Technology Meeting, IEEE Proceedings of the, 2006, , .	0.0	4
11	Comparison of Quasi-/Pseudo-Floating Gate Techniques and Low-Voltage Applications. Analog Integrated Circuits and Signal Processing, 2006, 47, 183-192.	1.4	19
12	Leveraged Current Mirror Op Amp. Analog Integrated Circuits and Signal Processing, 2003, 35, 25-31.	1.4	1
13	Dynamic Current Requirements in Single-Ended Log-Domain Filters. Analog Integrated Circuits and Signal Processing, 2001, 28, 73-82.	1.4	0