

Hasnain Lakdawala

List of Publications by Year in descending order

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8
papers

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citations

1684188

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327
citing authors

#	ARTICLE	IF	CITATIONS
1	A Transformer-Combined 31.5 dBm Outphasing Power Amplifier in 45 nm LP CMOS With Dynamic Power Control for Back-Off Power Efficiency Enhancement. IEEE Journal of Solid-State Circuits, 2012, 47, 1646-1658.	5.4	78
2	A 2.4-GHz 20-40-MHz Channel WLAN Digital Outphasing Transmitter Utilizing a Delay-Based Wideband Phase Modulator in 32-nm CMOS. IEEE Journal of Solid-State Circuits, 2012, 47, 3184-3196.	5.4	79
3	Smart integrated temperature sensor - mixed-signal circuits and systems in 32-nm and beyond. , 2011, , .		19
4	A 1.05 V 1.6 mW, 0.45 σ Resolution $\Sigma\Delta$ Based Temperature Sensor With Parasitic Resistance Compensation in 32 nm Digital CMOS Process. IEEE Journal of Solid-State Circuits, 2009, 44, 3621-3630.	5.4	67
5	A Class-E PA With Pulse-Width and Pulse-Position Modulation in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2009, 44, 1668-1678.	5.4	98
6	A 28.6dBm 65nm Class-E PA with Envelope Restoration by Pulse-Width and Pulse-Position Modulation. , 2008, , .		10
7	Temperature stabilization of CMOS capacitive accelerometers. Journal of Micromechanics and Microengineering, 2004, 14, 559-566.	2.6	41
8	A 0.5 mm ² /integrated capacitive vibration sensor with sub-10 zF/rt-Hz noise floor. , 0, , .		6