

Esteban Tlelo-Cuautle

List of Publications by Citations

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

245
papers

2,973
citations

31
h-index

45
g-index

285
ext. papers

3,620
ext. citations

2
avg, IF

5.89
L-index

#	Paper	IF	Citations
245	FPGA realization of multi-scroll chaotic oscillators. <i>Communications in Nonlinear Science and Numerical Simulation</i> , 2015 , 27, 66-80	3.7	151
244	FPGA realization of a chaotic communication system applied to image processing. <i>Nonlinear Dynamics</i> , 2015 , 82, 1879-1892	5	94
243	Integrated circuit generating 3- and 5-scroll attractors. <i>Communications in Nonlinear Science and Numerical Simulation</i> , 2012 , 17, 4328-4335	3.7	88
242	. <i>IEEE Transactions on Circuits and Systems I: Regular Papers</i> , 2011 , 58, 1382-1395	3.9	84
241	Generating a 50-scroll chaotic attractor at 66 MHz by using FPGAs. <i>Nonlinear Dynamics</i> , 2016 , 85, 2143-2157	3.57	79
240	Arduino-based chaotic secure communication system using multi-directional multi-scroll chaotic oscillators. <i>Nonlinear Dynamics</i> , 2017 , 87, 2203-2217	5	73
239	Hardware implementation of pseudo-random number generators based on chaotic maps. <i>Nonlinear Dynamics</i> , 2017 , 90, 1661-1670	5	70
238	N-scroll chaotic attractors from saturated function series employing CCII+s. <i>Nonlinear Dynamics</i> , 2010 , 61, 331-341	5	69
237	Frequency limitations in generating multi-scroll chaotic attractors using CFOAs. <i>International Journal of Electronics</i> , 2014 , 101, 1559-1569	1.2	66
236	A 3-D Multi-Stable System With a Peanut-Shaped Equilibrium Curve: Circuit Design, FPGA Realization, and an Application to Image Encryption. <i>IEEE Access</i> , 2020 , 8, 137116-137132	3.5	64
235	A survey on the integrated design of chaotic oscillators. <i>Applied Mathematics and Computation</i> , 2013 , 219, 5113-5122	2.7	62
234	Dynamics, FPGA realization and application of a chaotic system with an infinite number of equilibrium points. <i>Nonlinear Dynamics</i> , 2017 , 89, 1129-1139	5	60
233	CHAOTIC COMMUNICATION SYSTEM USING CHUA'S OSCILLATORS REALIZED WITH CCII+s. <i>International Journal of Bifurcation and Chaos in Applied Sciences and Engineering</i> , 2009 , 19, 4217-4226	2	56
232	Randomness improvement of chaotic maps for image encryption in a wireless communication scheme using PIC-microcontroller via Zigbee channels. <i>Chaos, Solitons and Fractals</i> , 2020 , 133, 109646	9.3	51
231	New alternatives for analog implementation of fractional-order integrators, differentiators and PID controllers based on integer-order integrators. <i>Nonlinear Dynamics</i> , 2017 , 90, 241-256	5	46
230	A New Chaotic System With Stable Equilibrium: From Theoretical Model to Circuit Implementation. <i>IEEE Access</i> , 2017 , 5, 8851-8858	3.5	45
229	FPGA-based implementation of different families of fractional-order chaotic oscillators applying Gr̄wald̄etnikov method. <i>Communications in Nonlinear Science and Numerical Simulation</i> , 2019 , 72, 516-527	3.7	43

228	Optimizing the maximum Lyapunov exponent and phase space portraits in multi-scroll chaotic oscillators. <i>Nonlinear Dynamics</i> , 2014 , 76, 1503-1515	5	43
227	Implementing a chaotic cryptosystem in a 64-bit embedded system by using multiple-precision arithmetic. <i>Nonlinear Dynamics</i> , 2019 , 96, 497-516	5	42
226	A Novel Chaotic System with Two Circles of Equilibrium Points: Multistability, Electronic Circuit and FPGA Realization. <i>Electronics (Switzerland)</i> , 2019 , 8, 1211	2.6	41
225	Multiscroll floating gate-based integrated chaotic oscillator. <i>International Journal of Circuit Theory and Applications</i> , 2013 , 41, 831-843	2	40
224	Symbolic analysis of analog circuits containing voltage mirrors and current mirrors. <i>Analog Integrated Circuits and Signal Processing</i> , 2010 , 65, 89-95	1.2	39
223	Design of Analog Circuits through Symbolic Analysis 2012 ,		34
222	FPAAs-based implementation of fractional-order chaotic oscillators using first-order active filter blocks. <i>Journal of Advanced Research</i> , 2020 , 25, 77-85	13	33
221	A switched-capacitor skew-tent map implementation for random number generation. <i>International Journal of Circuit Theory and Applications</i> , 2017 , 45, 305-315	2	32
220	Chaotic Image Encryption Using Hopfield and Hindmarsh-Rose Neurons Implemented on FPGA. <i>Sensors</i> , 2020 , 20,	3.8	32
219	Optimizing the positive Lyapunov exponent in multi-scroll chaotic oscillators with differential evolution algorithm. <i>Applied Mathematics and Computation</i> , 2013 , 219, 8163-8168	2.7	32
218	Richardson extrapolation-based sensitivity analysis in the multi-objective optimization of analog circuits. <i>Applied Mathematics and Computation</i> , 2013 , 222, 167-176	2.7	32
217	Implementation of a chaotic oscillator by designing Chua's diode with CMOS CFOAs. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 48, 159-162	1.2	32
216	Applications of Evolutionary Algorithms in the Design Automation of Analog Integrated Circuits. <i>Journal of Applied Sciences</i> , 2010 , 10, 1859-1872	0.3	32
215	Optimization and CMOS design of chaotic oscillators robust to PVT variations: INVITED. <i>The Integration VLSI Journal</i> , 2019 , 65, 32-42	1.4	32
214	FPGA-based Chaotic Cryptosystem by Using Voice Recognition as Access Key. <i>Electronics (Switzerland)</i> , 2018 , 7, 414	2.6	31
213	Binary Genetic Encoding for the Synthesis of Mixed-Mode Circuit Topologies. <i>Circuits, Systems, and Signal Processing</i> , 2012 , 31, 849-863	2.2	29
212	FPGA-based implementation of chaotic oscillators by applying the numerical method based on trigonometric polynomials. <i>AIP Advances</i> , 2018 , 8, 075217	1.5	28
211	Automatic Synthesis of VFs and VMs by Applying Genetic Algorithms. <i>Circuits, Systems, and Signal Processing</i> , 2008 , 27, 391-403	2.2	26

210	Analog/Digital Implementation of Fractional Order Chaotic Circuits and Applications 2020 ,		26
209	Engineering Applications of FPGAs 2016 ,		25
208	On the synchronization techniques of chaotic oscillators and their FPGA-based implementation for secure image transmission. <i>PLoS ONE</i> , 2019 , 14, e0209618	3.7	24
207	Development and implementation of a fish counter by using an embedded system. <i>Computers and Electronics in Agriculture</i> , 2018 , 145, 53-62	6.5	23
206	VHDL Descriptions for the FPGA Implementation of PWL-Function-Based Multi-Scroll Chaotic Oscillators. <i>PLoS ONE</i> , 2016 , 11, e0168300	3.7	22
205	Generalized admittance matrix models of OTRAs and COAs. <i>Microelectronics Journal</i> , 2010 , 41, 502-505	1.8	21
204	Optimising operational amplifiers by evolutionary algorithms and gm/Id method. <i>International Journal of Electronics</i> , 2016 , 103, 1665-1684	1.2	20
203	Implementing a Chaotic Cryptosystem by Performing Parallel Computing on Embedded Systems with Multiprocessors. <i>Entropy</i> , 2019 , 21,	2.8	18
202	FPGA-Based Implementation of a Multilayer Perceptron Suitable for Chaotic Time Series Prediction. <i>Technologies</i> , 2018 , 6, 90	2.4	18
201	CCII+ Based on QFGMOS for Implementing Chua's Chaotic Oscillator. <i>IEEE Latin America Transactions</i> , 2015 , 13, 2865-2870	0.7	17
200	Synthesis of CCII-s by superimposing VFs and CFs through genetic operations. <i>IEICE Electronics Express</i> , 2008 , 5, 411-417	0.5	17
199	Symbolic Analysis of OTRAs-Based Circuits. <i>Journal of Applied Research and Technology</i> , 2011 , 9,	1.7	17
198	A 5-D Multi-Stable Hyperchaotic Two-Disk Dynamo System With No Equilibrium Point: Circuit Design, FPGA Realization and Applications to TRNGs and Image Encryption. <i>IEEE Access</i> , 2021 , 9, 81352-81369	3.5	17
197	Thermal-Sensor-Based Occupancy Detection for Smart Buildings Using Machine-Learning Methods. <i>ACM Transactions on Design Automation of Electronic Systems</i> , 2018 , 23, 1-21	1.5	16
196	Operating-point driven formulation for analog computer-aided design. <i>Analog Integrated Circuits and Signal Processing</i> , 2013 , 74, 345-353	1.2	16
195	On the Relation between the Number of Scrolls and the Lyapunov Exponents in PWL-functions-based \mathbb{E} Scroll Chaotic Oscillators. <i>International Journal of Nonlinear Sciences and Numerical Simulation</i> , 2010 , 11,	1.8	15
194	Frequency scaling simulation of Chua's circuit by automatic determination and control of step-size. <i>Applied Mathematics and Computation</i> , 2007 , 194, 486-491	2.7	15
193	Evolutionary Electronics: Automatic Synthesis of Analog Circuits by GAs. <i>Studies in Computational Intelligence</i> , 2008 , 165-187	0.8	15

192	Analysis of a 4-D Hyperchaotic Fractional-Order Memristive System with Hidden Attractors. <i>Studies in Computational Intelligence</i> , 2017 , 207-235	0.8	14
191	Symbolic analysis of (MO)(I)CCI(II)(III)-based analog circuits. <i>International Journal of Circuit Theory and Applications</i> , 2009 , 38, n/a-n/a	2	14
190	Numerical Simulation of Chua's Circuit Oriented to Circuit Synthesis. <i>International Journal of Nonlinear Sciences and Numerical Simulation</i> , 2007 , 8,	1.8	14
189	Designing SRCOs by combining SPICE and Verilog-A. <i>International Journal of Electronics</i> , 2007 , 94, 373-379	2	14
188	Optimizing the Kaplan-Yorke Dimension of Chaotic Oscillators Applying DE and PSO. <i>Technologies</i> , 2019 , 7, 38	2.4	13
187	Optimizing current conveyors by evolutionary algorithms including differential evolution 2009 ,		13
186	On the trade-off between the number of scrolls and the operating frequency of the chaotic attractors 2011 ,		13
185	Behavioral model generation for symbolic analysis of analog integrated circuits		13
184	Design of Current-Mode Gm-C Filters from the Transformation of Opamp-RC Filters. <i>Journal of Applied Sciences</i> , 2007 , 7, 1321-1326	0.3	13
183	Optimization of the Kaplan-Yorke dimension in fractional-order chaotic oscillators by metaheuristics. <i>Applied Mathematics and Computation</i> , 2021 , 394, 125831	2.7	13
182	Synchronization of complex networks of identical and nonidentical chaotic systems via model-matching control. <i>PLoS ONE</i> , 2019 , 14, e0216349	3.7	12
181	Experimental Verification of Optimized Multiscroll Chaotic Oscillators Based on Irregular Saturated Functions. <i>Complexity</i> , 2018 , 2018, 1-17	1.6	12
180	Simulation of Piecewise-Linear One-Dimensional Chaotic Maps by Verilog-A. <i>IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India)</i> , 2015 , 32, 304-310	1.5	12
179	Synthesis of VFs and CFs by Manipulation of Generic Cells. <i>Analog Integrated Circuits and Signal Processing</i> , 2006 , 46, 99-102	1.2	12
178	On the Computational Synthesis of CMOS Voltage Followers. <i>IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences</i> , 2005 , E88-A, 3479-3484	0.4	12
177	PVT-Robust CMOS Programmable Chaotic Oscillator: Synchronization of Two 7-Scroll Attractors. <i>Electronics (Switzerland)</i> , 2018 , 7, 252	2.6	12
176	Sizing CMOS Amplifiers by PSO and MOL to Improve DC Operating Point Conditions. <i>Electronics (Switzerland)</i> , 2020 , 9, 1027	2.6	11
175	Simulation-based optimization of UGCs performances 2008 ,		11

174	Synchronization of chaotic artificial neurons and its application to secure image transmission under MQTT for IoT protocol. <i>Nonlinear Dynamics</i> , 2021 , 104, 4581	5	11
173	Design and Construction of an ROV for Underwater Exploration. <i>Sensors</i> , 2019 , 19,	3.8	11
172	CDCTA and OTA Realizations of a Multi-phase Sinusoidal Oscillator. <i>IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India)</i> , 2015 , 32, 497-504	1.5	10
171	Application of a Chaotic Oscillator in an Autonomous Mobile Robot. <i>Journal of Electrical Engineering</i> , 2014 , 65, 157-162	0.6	10
170	Maximizing Lyapunov Exponents in a Chaotic Oscillator by Applying Differential Evolution. <i>International Journal of Nonlinear Sciences and Numerical Simulation</i> , 2014 , 15,	1.8	10
169	Behavioral Modeling of SNFS for Synthesizing Multi-Scroll Chaotic Attractors. <i>International Journal of Nonlinear Sciences and Numerical Simulation</i> , 2013 , 14, 463-469	1.8	10
168	Simulation of Chua's circuit by automatic control of step-size. <i>Applied Mathematics and Computation</i> , 2007 , 190, 1526-1533	2.7	10
167	Automatic synthesis of 2D-n-scrolls chaotic systems by behavioral modeling. <i>Journal of Applied Research and Technology</i> , 2009 , 7,	1.7	10
166	Sizing Analog Integrated Circuits by Current-Branches-Bias Assignments with Heuristics. <i>Elektronika Ir Elektrotehnika</i> , 2013 , 19,	1.7	10
165	On the Electronic Realizations of Fractional-Order Phase-Lead-Lag Compensators with OpAmps and FPAA's. <i>Studies in Computational Intelligence</i> , 2017 , 131-164	0.8	10
164	Graph-Based Symbolic Technique for Improving Sensitivity Analysis in Analog Integrated Circuits. <i>IEEE Latin America Transactions</i> , 2014 , 12, 871-876	0.7	9
163	Symbolic behavioral model generation of current-mode analog circuits 2009 ,		9
162	Symbolic formulation method for mixed-mode analog circuits using nullors 2009 ,		9
161	SIASCA: Interactive System for the Symbolic Analysis of Analog Circuits. <i>IEICE Electronics Express</i> , 2004 , 1, 19-23	0.5	9
160	Pipeline FPGA-Based Implementations of ANNs for the Prediction of up to 600-Steps-Ahead of Chaotic Time Series. <i>Journal of Circuits, Systems and Computers</i> , 2021 , 30, 2150164	0.9	9
159	Development of a Portable, Reliable and Low-Cost Electrical Impedance Tomography System Using an Embedded System. <i>Electronics (Switzerland)</i> , 2021 , 10, 15	2.6	9
158	Issues on Applying One- and Multi-Step Numerical Methods to Chaotic Oscillators for FPGA Implementation. <i>Mathematics</i> , 2021 , 9, 151	2.3	9
157	Decomposition-based multi-objective optimization of second-generation current conveyors 2009 ,		8

156	Traffic Flow Prediction for Smart Traffic Lights Using Machine Learning Algorithms. <i>Technologies</i> , 2022 , 10, 5	2.4	8
155	Designing an authenticated Hash function with a 2D chaotic map. <i>Nonlinear Dynamics</i> , 2021 , 104, 4569	5	8
154	Memory Circuit Elements: Complexity, Complex Systems, and Applications. <i>Complexity</i> , 2019 , 2019, 1-4	1.6	8
153	An RBF-PSO technique for the rapid optimization of (CMOS) analog circuits 2018 ,		7
152	Experimental Synchronization of two Integrated Multi-scroll Chaotic Oscillators. <i>Journal of Applied Research and Technology</i> , 2014 , 12, 459-470	1.7	7
151	Synthesis of n-scroll attractors using saturated functions from high-level simulation. <i>Journal of Physics: Conference Series</i> , 2008 , 96, 012050	0.3	7
150	Automatic biasing and sizing of CMOS analog integrated circuits 2005 ,		7
149	Synthesis of Analog Circuits by Genetic Algorithms and their Optimization by Particle Swarm Optimization 2010 , 173-192		7
148	A New 4-D Multi-Stable Hyperchaotic System With No Balance Point: Bifurcation Analysis, Circuit Simulation, FPGA Realization and Image Cryptosystem. <i>IEEE Access</i> , 2021 , 9, 144555-144573	3.5	7
147	Estimating the Highest Time-Step in Numerical Methods to Enhance the Optimization of Chaotic Oscillators. <i>Mathematics</i> , 2021 , 9, 1938	2.3	7
146	On maximizing the positive Lyapunov exponent of chaotic oscillators applying DE and PSO. <i>International Journal of Dynamics and Control</i> , 2019 , 7, 1157-1172	1.7	6
145	Sizing Analogue Integrated Circuits by Integer Encoding and NSGA-II. <i>IETE Technical Review (Institution of Electronics and Telecommunication Engineers, India)</i> , 2018 , 35, 237-243	1.5	6
144	OCBA in the yield optimization of analog integrated circuits by evolutionary algorithms 2015 ,		6
143	Modeling memory effects in RF power amplifiers applied to a digital pre-distortion algorithm and emulated on a DSP-FPGA board. <i>The Integration VLSI Journal</i> , 2015 , 49, 49-64	1.4	6
142	Performance bound analysis of analog circuits in frequency- and time-domain considering process variations. <i>ACM Transactions on Design Automation of Electronic Systems</i> , 2013 , 19, 1-22	1.5	6
141	Evolutionary Algorithms in the Optimal Sizing of Analog Circuits. <i>Studies in Computational Intelligence</i> , 2011 , 109-138	0.8	6
140	Simulation-based optimization of CCLIs' performances in weak inversion 2010 ,		6
139	Designing VFs by applying genetic algorithms from nullator-based descriptions 2007 ,		6

138	Synthesis of CCII _s and Design of Simulated CCII Based Floating Inductances 2007 ,		6
137	Real-time RGB image encryption for IoT applications using enhanced sequences from chaotic maps. <i>Chaos, Solitons and Fractals</i> , 2021 , 153, 111506	9.3	6
136	Design of a Wide-Band Voltage-Controlled Ring Oscillator Implemented in 180 nm CMOS Technology. <i>Electronics (Switzerland)</i> , 2019 , 8, 1156	2.6	6
135	Convergence rates of the efficient global optimization algorithm for improving the design of analog circuits. <i>Analog Integrated Circuits and Signal Processing</i> , 2020 , 103, 143-162	1.2	5
134	Symbolic Moment Computation for Statistical Analysis of Large Interconnect Networks. <i>IEEE Transactions on Very Large Scale Integration (VLSI) Systems</i> , 2013 , 21, 944-957	2.6	5
133	Sensitivity analysis in the optimal sizing of analog circuits by evolutionary algorithms 2010 ,		5
132	An efficient biasing technique suitable for any kind of the four basic amplifiers designed at nullor level		5
131	Biasing analog circuits using the nullor concept		5
130	Selection of MOSFET Sizes by Fuzzy Sets Intersection in the Feasible Solutions Space. <i>Journal of Applied Research and Technology</i> , 2012 , 10,	1.7	5
129	CMOS OTA-Based Filters for Designing Fractional-Order Chaotic Oscillators. <i>Fractal and Fractional</i> , 2021 , 5, 122	3	5
128	Gate Sizing Methodology with a Novel Accurate Metric to Improve Circuit Timing Performance under Process Variations. <i>Technologies</i> , 2020 , 8, 25	2.4	4
127	Investigation of Early Warning Indexes in a Three-Dimensional Chaotic System with Zero Eigenvalues. <i>Entropy</i> , 2020 , 22,	2.8	4
126	Expected Improvement-Based Optimization Approach for the Optimal Sizing of a CMOS Operational Transconductance Amplifier 2018 ,		4
125	Graph-Based Symbolic and Symbolic Sensitivity Analysis of Analog Integrated Circuits. <i>Lecture Notes in Electrical Engineering</i> , 2013 , 101-122	0.2	4
124	2017 ,		4
123	DDD-based symbolic sensitivity analysis of active filters 2012 ,		4
122	Symbolic nodal analysis of analog integrated circuits using pathological elements 2012 ,		4
121	Simulating the synchronization of multi-scroll chaotic oscillators 2013 ,		4

120	Design and Applications of Continuous-Time Chaos Generators 2011 ,		4
119	Simulation of Chua's chaotic oscillator using unity-gain cells 2008 ,		4
118	Designing Chua's circuit from the behavioral to the transistor level of abstraction. <i>Applied Mathematics and Computation</i> , 2007 , 184, 715-720	2.7	4
117	On the Sizing of CMOS Operational Amplifiers by Applying Many-Objective Optimization Algorithms. <i>Electronics (Switzerland)</i> , 2021 , 10, 3148	2.6	4
116	SODAC. <i>International Journal of Applied Metaheuristic Computing</i> , 2012 , 3, 64-83	0.8	4
115	Hyperchaotic Encryption for Secure E-Mail Communication. <i>Advanced Information and Knowledge Processing</i> , 2010 , 471-486	0.3	4
114	FPGA Realization of Spherical Chaotic System with Application in Image Transmission. <i>Mathematical Problems in Engineering</i> , 2021 , 2021, 1-16	1.1	4
113	Fractional-Order Approximation of PID Controller for Buck-Boost Converters. <i>Micromachines</i> , 2021 , 12,	3.3	4
112	Enhancing Q-Factor in a Biquadratic Bandpass Filter Implemented with Opamps. <i>Technologies</i> , 2019 , 7, 64	2.4	4
111	On the Prediction of the Threshold Voltage Degradation in CMOS Technology Due to Bias-Temperature Instability. <i>Electronics (Switzerland)</i> , 2018 , 7, 427	2.6	4
110	Pseudo Expected Improvement Based-Optimization for CMOS Analog Circuit Design 2019 ,		3
109	FPGA Implementation of Chaotic Oscillators, Their Synchronization, and Application to Secure Communications 2019 , 301-328		3
108	Optimizing an LDO voltage regulator by evolutionary algorithms considering tolerances of the circuit elements 2015 ,		3
107	Application of Computational Intelligence Techniques to Maximize Unpredictability in Multiscroll Chaotic Oscillators 2015 , 59-81		3
106	Prediction of chaotic time series by using ANNs, ANFIS and SVMs 2018 ,		3
105	Experimental realization of a multiscroll chaotic oscillator with optimal maximum Lyapunov exponent. <i>Scientific World Journal, The</i> , 2014 , 2014, 303614	2.2	3
104	2011 ,		3
103	Chaos-based communication systems by applying Hamiltonian synchronization 2010 ,		3

102	Symbolic noise analysis of low voltage amplifiers by using nullors 2010 ,		3
101	Multiscroll oscillator based on floating gate CMOS inverter 2010 ,		3
100	Multi-scroll Chaotic Oscillator Employing UGCs 2009 ,		3
99	Fuzzy-set based approach to compute optimum sizes of Voltage Followers 2009 ,		3
98	A CAD-tool for the design of n-scrolls chaotic systems from behavioral modeling 2009 ,		3
97	Modeling and Simulation of a Parallel Mechanical Elbow with 3 DOF 2008 ,		3
96	Symbolic Noise Analysis in Gm-C Filters 2006 ,		3
95	Low-Voltage Chaotic Oscillator using Voltage and Current Followers 2007 ,		3
94	Symbolic noise analysis in analog integrated circuits		3
93	Simulation of a parallel mechanical elbow with 3 DOF. <i>Journal of Applied Research and Technology</i> , 2009 , 7,	1.7	3
92	Integer and Fractional-Order Chaotic Circuits and Systems 2020 , 1-40		3
91	Single-Objective Optimization of a CMOS VCO Considering PVT and Monte Carlo Simulations. <i>Mathematical and Computational Applications</i> , 2020 , 25, 76	1	3
90	Maximizing the Chaotic Behavior of Fractional Order Chen System by Evolutionary Algorithms. <i>Mathematics</i> , 2021 , 9, 1194	2.3	3
89	Mathematical and numerical analysis of the dynamical behavior of chen oscillator. <i>International Journal of Dynamics and Control</i> , 2020 , 8, 386-395	1.7	3
88	A Memristive System with Hidden Attractors and Its Engineering Application. <i>Studies in Computational Intelligence</i> , 2017 , 81-99	0.8	2
87	Study of regression methodologies on analog circuit design 2015 ,		2
86	Metamodelling Techniques for the Optimal Design of Low-Noise Amplifiers. <i>Electronics (Switzerland)</i> , 2020 , 9, 787	2.6	2
85	FPGA-based system for effective IQ imbalance mitigation of RF power amplifiers. <i>International Journal of Circuit Theory and Applications</i> , 2020 , 48, 512-523	2	2

84	Optimization of LDO voltage regulators by NSGA-II 2016 ,		2
83	A new four-dimensional chaotic system with hidden attractor and its circuit design 2018 ,		2
82	Kriging Metamodeling-Assisted Multi-Objective Optimization of CMOS Current Conveyors 2018 ,		2
81	High-Q and Wide-Bandwidth Capacitor Multiplier Optimized by NSGA-II. <i>IETE Journal of Research</i> , 2019 , 65, 661-666	0.9	2
80	Performance bound and yield analysis for analog circuits under process variations 2013 ,		2
79	Comprehensive detection of counterfeit ICs via on-chip sensor and post-fabrication authentication policy 2017 ,		2
78	Optimizing operational amplifiers by metaheuristics and considering tolerance analysis 2015 ,		2
77	A new segmentation-based GPU-accelerated sparse matrix-vector multiplication 2014 ,		2
76	Statistical extraction and modeling of inductance considering spatial correlation. <i>Analog Integrated Circuits and Signal Processing</i> , 2012 , 73, 3-11	1.2	2
75	Sensitivity analysis in the optimal sizing of analog ICs by evolutionary algorithms 2013 ,		2
74	Statistical extraction and modeling of 3-D inductance with spatial correlation 2010 ,		2
73	Multi-objective simulation-based optimization for the optimal design of analog circuits 2011 ,		2
72	Symbolic DDD-based tool for the computation of noise in CMOS analog circuits 2012 ,		2
71	Non-sorting genetic algorithm in the optimization of unity-gain cells 2009 ,		2
70	Current conveyor realization of synchronized Chua's circuits for binary communications 2008 ,		2
69	Systematic design of CCI(II)(III)s by combining UGCs 2008 ,		2
68	Synchronization of n-scrolls chaotic systems synthesized from high-level behavioral modeling 2008 ,		2
67	Analog implementation of MOS-translinear Morlet Wavelets		2

66				2
65	Poincaré maps for detecting chaos in fractional-order systems with hidden attractors for its Kaplan-Yorke dimension optimization. <i>AIMS Mathematics</i> , 2022 , 7, 5871-5894		2.2	2
64	Kalman observers in estimating the states of chaotic neurons for image encryption under MQTT for IoT protocol. <i>European Physical Journal: Special Topics</i> , 1		2.3	2
63	Synchronization and Applications of Fractional-Order Chaotic Systems 2020 , 175-201			2
62	. <i>IEEE Access</i> , 2020 , 8, 138217-138235		3.5	2
61	A new four-dimensional two-scroll hyperchaos dynamical system with no rest point, bifurcation analysis, multi-stability, circuit simulation and FPGA design. <i>International Journal of Computer Applications in Technology</i> , 2020 , 63, 147		0.7	2
60	Automated Driving of GaN Chireix Power Amplifier for the Digital Predistortion Linearization. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2021 , 68, 1887-1891		3.5	2
59	Discrete Chaotic Dynamics for Economics and Social Science. <i>Discrete Dynamics in Nature and Society</i> , 2016 , 2016, 1-2		1.1	2
58	Review: Advances in BTI modeling for the design of reliable ICs 2016 ,			2
57	Kinematic Control of a Robot by Using a Non-autonomous Chaotic System. <i>Studies in Computational Intelligence</i> , 2016 , 1-17		0.8	2
56	Attention Measurement of an Autism Spectrum Disorder User Using EEG Signals: A Case Study. <i>Mathematical and Computational Applications</i> , 2022 , 27, 21		1	2
55	FPGA-based test bed for measurement of AM/AM and AM/PM distortion and modeling memory effects in RF PAs. <i>The Integration VLSI Journal</i> , 2016 , 52, 291-300		1.4	1
54	Fast and Efficient Sensitivity Aware Multi-Objective Optimization of Analog Circuits. <i>Technologies</i> , 2019 , 7, 40		2.4	1
53	Surrogate Assisted Optimization for Low-Voltage Low-Power Circuit Design. <i>Journal of Low Power Electronics and Applications</i> , 2020 , 10, 20		1.7	1
52	A novel chaotic system in the spherical coordinates. <i>European Physical Journal: Special Topics</i> , 2020 , 229, 1257-1263		2.3	1
51	Optimal Sizing of Amplifiers by Evolutionary Algorithms with Integer Encoding and (g _m /I _D) Design Method. <i>Studies in Computational Intelligence</i> , 2018 , 263-279		0.8	1
50	Linearizing the Transconductance of an OTA Through the Optimal Sizing by Applying NSGA-II 2018 ,			1
49	A New Nonlinear Dynamical Model with Three Quadratic Nonlinear Terms and Hidden Chaos 2019 ,			1

48	Prediction of chaotic time-series with different MLE values using FPGA-based ANNs 2017 ,		1
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