

Chutham

List of Publications by Year in descending order

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45
papers

479
citations

933447

10
h-index

1199594

12
g-index

45
all docs

45
docs citations

45
times ranked

274
citing authors

#	ARTICLE	IF	CITATIONS
1	A 77-dB DR 16-Ch 2 nd -order $\hat{\nu}$ - $\hat{\nu}$ Neural Recording Chip with 0.0077mm ² /Ch. , 2021, , .		0
2	A Compact Sub- $\frac{1}{4}$ W CMOS ECG Amplifier With 57.5-M \hat{a} , Z _{in} , 2.02 NEF, 8.16 PEF and 83.24-dB CMRR. IEEE Transactions on Biomedical Circuits and Systems, 2021, 15, 549-558.	4.0	21
3	A Compact Chopper Stabilized $\hat{\nu}$ - $\hat{\nu}$ Neural Readout IC With Input Impedance Boosting. IEEE Open Journal of the Solid-State Circuits Society, 2021, 1, 67-78.	2.7	12
4	A 63 nW, 250 Hz, 70 dB-DR, Subthreshold CMOS Follower-Based LPF for ECG Detection. , 2019, , .		1
5	A 0.672 $\frac{1}{4}$ W, 2 $\frac{1}{4}$ V _{rms} CMOS Current-Feedback ECG Pre-amplifier With 77 dB CMRR. , 2019, , .		6
6	An Improved FVF Lowpass Filter with 0.02 fJ-FoM. , 2019, , .		2
7	A Nanopower Biopotential Lowpass Filter Using Subthreshold Current-Reuse Biquads With Bulk Effect Self-Neutralization. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 1746-1757.	5.4	33
8	A Subthreshold Buffer-Based Biquadratic Cell and its Application to Biopotential Filter Design. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2774-2783.	5.4	42
9	A Compact Bulk-Driven Four-Quadrant Analog Multiplier in Weak Inversion. , 2018, , .		4
10	A 0.9-nW, 101-Hz, and 46.3- $\hat{\mu}$ V _{rms} IRN Low-Pass Filter for ECG Acquisition Using FVF Biquads. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2290-2298.	3.1	40
11	A 1.5 V 5.2 nW 60 dB-DR Lowpass Filter With Self-Compensated Gain in 0.35 $\frac{1}{4}$ m CMOS Suitable for Biomedical Applications. , 2018, , .		5
12	A Flipped Voltage Follower Second-Order Bandpass Filter. Journal of Circuits, Systems and Computers, 2017, 26, 1750112.	1.5	3
13	Performance enhancement of a subthreshold MOS folded-cascode OTA using enhanced lateral BJT input stage. , 2017, , .		0
14	A modified voltage-clamped ISFET readout circuit for low voltage applications. , 2017, , .		2
15	0.6-V, Sub-nW, second-order lowpass filters using flipped voltage followers. , 2016, , .		12
16	Flipped voltage follower ISFET readout circuits. , 2016, , .		2
17	To investigate how to design electric current driver and voltage measuring circuits of a conductance catheter for blood volume measurement. , 2015, , .		0
18	A compact subthreshold CMOS 2 nd -order g_m - C lowpass filter. , 2015, , .		0

#	ARTICLE	IF	CITATIONS
19	A hybrid OTA-C notch filter for physiological signal acquisition. , 2015, , .		3
20	A 9.19 nW hybrid OTA " Source follower biquad lowpass filter. , 2014, , .		10
21	A 1.5-V, 300-nW, continuous-time allpass filter With 67 dB dynamic range. , 2014, , .		2
22	A 0.5-V, 2-nW, 55-dB DR, fourth-order bandpass filter using single branch biquads: An efficient design for FoM enhancement. Microelectronics Journal, 2014, 45, 367-374.	2.0	29
23	A switched gain cell parametric amplifier. , 2013, , .		0
24	A 2.6nW, 0.5V, 52dB-DR, 4 th -order G _m /I _m -C BPF: Moving closer to the FoM's fundamental limit. , 2012, , .		3
25	A Compact Rail-to-Rail Class-AB CMOS Buffer With Slew-Rate Enhancement. IEEE Transactions on Circuits and Systems II: Express Briefs, 2012, 59, 486-490.	3.0	33
26	A modular transconductance reduction technique for very low-frequency G _m /I _m -C filters. , 2012, , .		16
27	A nano power CMOS tinnitus detector for a fully implantable closed-loop neurodevice. , 2011, , .		2
28	Analysis and Design of a Low-Voltage, Low-Power, High-Precision, Class-AB Current-Mode Subthreshold CMOS Sample and Hold Circuit. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 1615-1626.	5.4	21
29	A wide-input linear range sub-threshold transconductor for sub-Hz filtering. , 2010, , .		7
30	Analog complex gammatone filter for cochlear implant channels. , 2010, , .		16
31	A 24nW, 0.65-V, 74-dB SNDR, 83-dB DR, class-AB current-mode sample and hold circuit. , 2010, , .		5
32	A least-voltage drop high output resistance current source for neural stimulation. , 2010, , .		7
33	Comparison of speech processing strategies for the design of an ultra low-power analog bionic ear. , 2010, 2010, 1374-7.		3
34	An ultra low-power peak-instant detector for a peak picking cochlear implant processor. , 2010, , .		7
35	An additive instantaneously companding readout system for cochlear implants. , 2010, , .		3
36	A nano-power class-AB current multiplier for energy-based action potential detector. , 2009, , .		1

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37	Dynamic translinear nonlinear energy operator. , 2009, , .		6
38	Ultra-low-power, class-AB, CMOS four-quadrant current multiplier. Electronics Letters, 2009, 45, 483.	1.0	48
39	Nanopower sampled data wavelet filter design using Switched Gain Cell technique. , 2009, , .		5
40	A compact, nano-power CMOS action potential detector. , 2009, , .		9
41	A 1.5V, wide-input range, high-bandwidth, CMOS four-quadrant analog multiplier. , 2008, , .		16
42	Low-voltage, low-power, low switching error, class-AB switched current memory cell. Electronics Letters, 2008, 44, 706.	1.0	5
43	A low-power CMOS analog voltage buffer using compact adaptive biasing. , 2007, , .		12
44	A Compact High Current Efficiency Low-Voltage MOS Transconductor with Nearly Constant Input Voltage Range. , 2007, , .		4
45	Compact low-voltage CMOS four-quadrant analogue multiplier. Electronics Letters, 2006, 42, 1149.	1.0	21