

Soumya Eachempati

List of Publications by Year in descending order

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7
papers

317
citations

3311381

1
h-index

3475538

1
g-index

7
all docs

7
docs citations

7
times ranked

197
citing authors

#	ARTICLE	IF	CITATIONS
1	Design and evaluation of a hierarchical on-chip interconnect for next-generation CMPs. , 2009, , .		135
2	MIRA: A Multi-layered On-Chip Interconnect Router Architecture. , 2008, , .		76
3	Assessing Carbon Nanotube Bundle Interconnect for Future FPGA Architectures. , 2007, , .		54
4	Reconfigurable BDD based quantum circuits. , 2008, , .		25
5	RAFT: A router architecture with frequency tuning for on-chip networks. Journal of Parallel and Distributed Computing, 2011, 71, 625-640.	4.1	24
6	Optimizing power and performance for reliable on-chip networks. , 2010, , .		3
7	Power and area reduction using carbon nanotube bundle interconnect in global clock tree distribution network. , 2009, , .		0